

AP2 BPM status

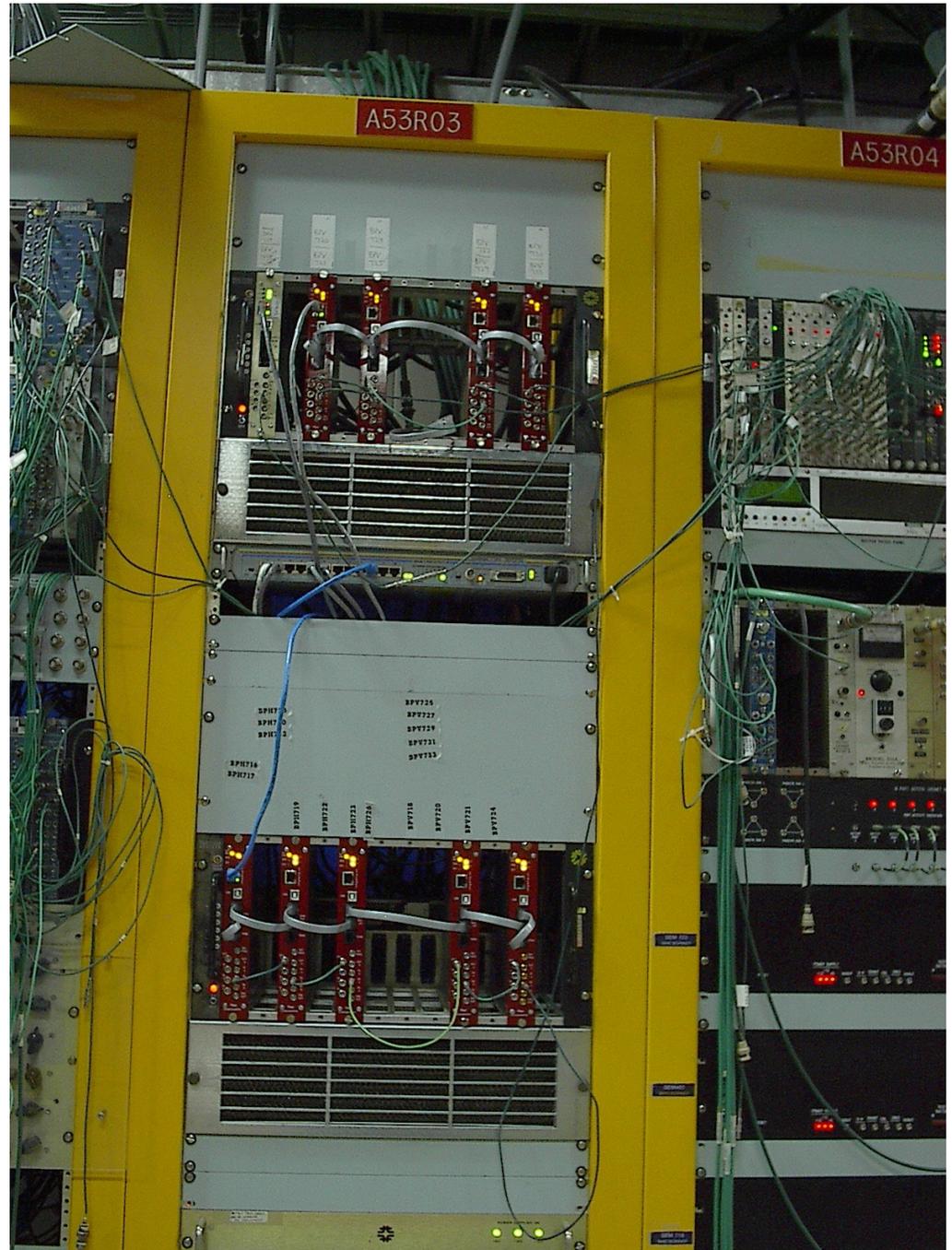
Bill Ashmanskas, Sten Hansen, Terry Kiper, Dave Peterson, 6/2/05

- 10/10 Rev C boards installed in AP50; 8/8 Rev C boards installed in F27.
- Have Acnet readout (OAC), data logging, etc.
- Jim Budlong has Java program ready.
- Will fill in D-to-A line with next 4 boards.
- We made 25. That leaves 3 spares + 5 Rev B boards. Will reprogram Rev B for D:BPI10D, etc.
- Nathan Eddy and I measured ~ 90um position resolution using AWG in his BPM test stand.
 - But his echotek boards get ~ 10um.
 - Best I've seen so far in the field is 300um.

F27



AP50 (last RevB has been replaced with RevC since photo)



To-do list

- Short term:
 - Migrate from DLLRF OAC to AP2BPM OAC.
 - Make OAC more robust against network glitches.
 - Check that every channel is reading out sensible data.
- Medium term:
 - Establish calibration scale factor for each channel.
 - Check stacking data against lattice model.
- Longer term:
 - Investigate alternative signal-processing schemes (just reprogramming same hardware) to try to get $\sim 50\mu\text{m}$ resolution

This document was created with Win2PDF available at <http://www.daneprairie.com>.
The unregistered version of Win2PDF is for evaluation or non-commercial use only.