From: **bellanto** bellanto@fnal.gov
Subject: Re: ORC Responses to 4 points
Date: October 28, 2015 at 3:25 PM
To: Ryan A Rivera rrivera@fnal.gov, Lorenzo Uplegger uplegger@fnal.gov, Yuri Gershtein gershtein@physics.rutgers.edu
Cc: Kathy J Graden graden@fnal.gov, Steve J Chappa chappa@fnal.gov, Madelyn Wolter maddiew@fnal.gov, David Mertz mertz@fnal.gov, James L. Priest priest@fnal.gov, Angela M Aparicio asands@fnal.gov, Karen M Kephart karenk@fnal.gov, Teri L Dykhuis dykhuis@fnal.gov, Robert J Bushek bushek@fnal.gov, Eric D McHugh emchugh@fnal.gov, Russell A Rucinski rucinski@fnal.gov, Gary L Lauten glauten@fnal.gov, Michael G. Herr michael.herr@science.doe.gov, Yun He yunhe@fnal.gov, Eugene E Schmidt Jr jj@fnal.gov, Mandy Kathleen Rominsky rominsky@fnal.gov

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Dear Yuri,

Thanks for the info.

Please elaborate on 4). As we both know, that unit does get pretty hot. How do you plan to tell if you don't need an FPGA fan apart from destroying the FPGA?

Leo Bellantoni
Scientist

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On Oct 28, 2015, at 3:16 PM, Ryan Rivera <rrivera@fnal.gov> wrote:

> Hello all,
>
> Please see responses below.
>
> Thanks - please let us know if further info is needed,
>
> Ryan Rivera
>
> ---------- Forwarded message ----------
> From: **Yuri Gershtein** <gershtein@physics.rutgers.edu>
> Date: Wed, Oct 28, 2015 at 3:08 PM
> Subject: schematics and answers
> To: Ryan A Rivera <rrivera@fnal.gov>
>
> 1) new power schematic drawing attached
>
> 2) fuse in XYPositioner is 10A as in schematics now
>
> 3) questions were passed along to designers, but unclear if we will hear from CERN soon. However, 12V is the raw power to entire FPGA card - FPGA power calculations from vendor indicate less than 10A will be drawn. GLIB is a CERN lab-wide general purpose DAQ board. It is used in CMS and in general in MicroTCA crates that are capable of delivering 10-30A @ 12V. The power supply we are using is limited to 10A only.
>
> 4) we will start with no FPGA fan installed. If it is determined we need to add a fan, we will send pic.

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