

Det	W	Phi	tower	pixel	RM	Card	QIE	Qfib	Crate	Hslot	HTR	Hfib	Hch	FPGA	Dslot	DCC	Dch
HB minus	7	23	9	11	1	3	3	6	4	2	0	0	2	T	9	7	0
HB minus	7	23	5	6	1	3	4	6	4	2	0	0	0	T	9	7	0
HB minus	7	23	1	3	1	3	5	6	4	2	0	0	1	T	9	7	0
HB minus	7	23	10	9	1	2	0	3	4	2	0	1	0	T	9	7	0
HB minus	7	23	6	5	1	2	1	3	4	2	0	1	1	T	9	7	0
HB minus	7	23	2	2	1	2	2	3	4	2	0	1	2	T	9	7	0
HB minus	7	24	9	12	2	3	3	6	4	2	0	2	2	T	9	7	0
HB minus	7	24	5	16	2	3	4	6	4	2	0	2	0	T	9	7	0
HB minus	7	24	1	18	2	3	5	6	4	2	0	2	1	T	9	7	0
HB minus	7	24	10	4	2	2	0	3	4	2	0	3	0	T	9	7	0
HB minus	7	24	6	8	2	2	1	3	4	2	0	3	1	T	9	7	0
HB minus	7	24	2	13	2	2	2	3	4	2	0	3	2	T	9	7	0
HB minus	7	25	9	8	3	3	3	6	4	2	0	4	2	T	9	7	0
HB minus	7	25	5	4	3	3	4	6	4	2	0	4	0	T	9	7	0
HB minus	7	25	1	2	3	3	5	6	4	2	0	4	1	T	9	7	0
HB minus	7	25	10	16	3	2	0	3	4	2	0	5	0	T	9	7	0
HB minus	7	25	6	12	3	2	1	3	4	2	0	5	1	T	9	7	0
HB minus	7	25	2	7	3	2	2	3	4	2	0	5	2	T	9	7	0
HB minus	7	26	9	9	4	3	3	6	4	2	0	6	2	T	9	7	0
HB minus	7	26	5	14	4	3	4	6	4	2	0	6	0	T	9	7	0
HB minus	7	26	1	17	4	3	5	6	4	2	0	6	1	T	9	7	0
HB minus	7	26	10	11	4	2	0	3	4	2	0	7	0	T	9	7	0
HB minus	7	26	6	15	4	2	1	3	4	2	0	7	1	T	9	7	0
HB minus	7	26	2	18	4	2	2	3	4	2	0	7	2	T	9	7	0
HB minus	7	23	12	16	1	2	3	4	4	2	0	0	2	B	9	7	0
HB minus	7	23	8	12	1	2	4	4	4	2	0	0	0	B	9	7	0
HB minus	7	23	4	7	1	2	5	4	4	2	0	0	1	B	9	7	0
HB minus	7	23	11	8	1	3	0	5	4	2	0	1	0	B	9	7	0
HB minus	7	23	7	4	1	3	1	5	4	2	0	1	1	B	9	7	0
HB minus	7	23	3	1	1	3	2	5	4	2	0	1	2	B	9	7	0
HB minus	7	24	12	6	2	2	3	4	4	2	0	2	2	B	9	7	0
HB minus	7	24	8	11	2	2	4	4	4	2	0	2	0	B	9	7	0
HB minus	7	24	4	15	2	2	5	4	4	2	0	2	1	B	9	7	0
HB minus	7	24	11	5	2	3	0	5	4	2	0	3	0	B	9	7	0
HB minus	7	24	7	9	2	3	1	5	4	2	0	3	1	B	9	7	0
HB minus	7	24	3	14	2	3	2	5	4	2	0	3	2	B	9	7	0
HB minus	7	25	12	14	3	2	3	4	4	2	0	4	2	B	9	7	0
HB minus	7	25	8	9	3	2	4	4	4	2	0	4	0	B	9	7	0
HB minus	7	25	4	5	3	2	5	4	4	2	0	4	1	B	9	7	0
HB minus	7	25	11	15	3	3	0	5	4	2	0	5	0	B	9	7	0
HB minus	7	25	7	11	3	3	1	5	4	2	0	5	1	B	9	7	0
HB minus	7	25	3	6	3	3	2	5	4	2	0	5	2	B	9	7	0
HB minus	7	26	12	4	4	2	3	4	4	2	0	6	2	B	9	7	0
HB minus	7	26	8	8	4	2	4	4	4	2	0	6	0	B	9	7	0
HB minus	7	26	4	13	4	2	5	4	4	2	0	6	1	B	9	7	0
HB minus	7	26	11	12	4	3	0	5	4	2	0	7	0	B	9	7	0
HB minus	7	26	7	16	4	3	1	5	4	2	0	7	1	B	9	7	0
HB minus	7	26	3	19	4	3	2	5	4	2	0	7	2	B	9	7	0

Det	W	Phi	tower	pixel	RM	Card	QIE	Qfib	Crate	Hslot	HTR	Hfib	Hch	FPGA	Dslot	DCC	Dch
HB minus	7	23	16r	18	1	1	0	1	4	3	1	0	0	T	9	7	1
HB minus	7	23	15r	13	1	1	1	1	4	3	1	0	1	T	9	7	1
HB minus	7	23	14	14	1	1	2	1	4	3	1	0	2	T	9	7	1
HB minus	7	23	16f	17	1	1	3	2	4	3	1	1	2	T	9	7	1
HB minus	7	23	15f	19	1	1	4	2	4	3	1	1	0	T	9	7	1
HB minus	7	23	13	15	1	1	5	2	4	3	1	1	1	T	9	7	1
HB minus	7	24	16r	17	2	1	0	1	4	3	1	2	0	T	9	7	1
HB minus	7	24	15r	3	2	1	1	1	4	3	1	2	1	T	9	7	1
HB minus	7	24	14	1	2	1	2	1	4	3	1	2	2	T	9	7	1
HB minus	7	24	16f	19	2	1	3	2	4	3	1	3	2	T	9	7	1
HB minus	7	24	15f	2	2	1	4	2	4	3	1	3	0	T	9	7	1
HB minus	7	24	13	7	2	1	5	2	4	3	1	3	1	T	9	7	1
HE minus	7	23	17	12	1	1	0	1	4	3	1	4	0	T	9	7	1
HE minus	7	23	16	16	1	1	1	1	4	3	1	4	1	T	9	7	1
HE minus	7	23-24	26f	7	1	1	2	1	4	3	1	4	2	T	9	7	1
HE minus	7	23-24	26r	3	1	1	3	2	4	3	1	5	2	T	9	7	1
HE minus	7	23	18f	11	1	1	4	2	4	3	1	5	0	T	9	7	1
HE minus	7	23	18r	19	1	1	5	2	4	3	1	5	1	T	9	7	1
HE minus	7	24	17	12	2	1	0	1	4	3	1	6	0	T	9	7	1
HE minus	7	24	16	7	2	1	1	1	4	3	1	6	1	T	9	7	1
HE minus	7	23-24	25f	16	2	1	2	1	4	3	1	6	2	T	9	7	1
HE minus	7	23-24	25r	19	2	1	3	2	4	3	1	7	2	T	9	7	1
HE minus	7	24	18f	11	2	1	4	2	4	3	1	7	0	T	9	7	1
HE minus	7	24	18r	3	2	1	5	2	4	3	1	7	1	T	9	7	1
HB minus	7	25	16r	3	3	1	0	1	4	3	1	0	0	B	9	7	1
HB minus	7	25	15r	17	3	1	1	1	4	3	1	0	1	B	9	7	1
HB minus	7	25	14	19	3	1	2	1	4	3	1	0	2	B	9	7	1
HB minus	7	25	16f	1	3	1	3	2	4	3	1	1	2	B	9	7	1
HB minus	7	25	15f	18	3	1	4	2	4	3	1	1	0	B	9	7	1
HB minus	7	25	13	13	3	1	5	2	4	3	1	1	1	B	9	7	1
HB minus	7	26	16r	2	4	1	0	1	4	3	1	2	0	B	9	7	1
HB minus	7	26	15r	7	4	1	1	1	4	3	1	2	1	B	9	7	1
HB minus	7	26	14	6	4	1	2	1	4	3	1	2	2	B	9	7	1
HB minus	7	26	16f	3	4	1	3	2	4	3	1	3	2	B	9	7	1
HB minus	7	26	15f	1	4	1	4	2	4	3	1	3	0	B	9	7	1
HB minus	7	26	13	5	4	1	5	2	4	3	1	3	1	B	9	7	1
HE minus	7	25	17	12	3	1	0	1	4	3	1	4	0	B	9	7	1
HE minus	7	25	16	16	3	1	1	1	4	3	1	4	1	B	9	7	1
HE minus	7	25-26	25f	7	3	1	2	1	4	3	1	4	2	B	9	7	1
HE minus	7	25-26	25r	3	3	1	3	2	4	3	1	5	2	B	9	7	1
HE minus	7	25	18f	11	3	1	4	2	4	3	1	5	0	B	9	7	1
HE minus	7	25	18r	19	3	1	5	2	4	3	1	5	1	B	9	7	1
HE minus	7	26	17	12	4	1	0	1	4	3	1	6	0	B	9	7	1
HE minus	7	26	16	7	4	1	1	1	4	3	1	6	1	B	9	7	1
HE minus	7	25-26	26f	16	4	1	2	1	4	3	1	6	2	B	9	7	1
HE minus	7	25-26	26r	19	4	1	3	2	4	3	1	7	2	B	9	7	1
HE minus	7	26	18f	11	4	1	4	2	4	3	1	7	0	B	9	7	1
HE minus	7	26	18r	3	4	1	5	2	4	3	1	7	1	B	9	7	1

Det	W	Phi tower	pixel	RM	Card	QIE	Qfib	Crate	Hslot	HTR	Hfib	Hch	FPGA	Dslot	DCC	Dch	
HE minus	7	23	19r	17	1	3	0	5	4	4	2	0	0	T	9	7	2
HE minus	7	23	20f	9	1	3	1	5	4	4	2	0	1	T	9	7	2
HE minus	7	23-24	22r	4	1	3	2	5	4	4	2	0	2	T	9	7	2
HE minus	7	23	19f	13	1	3	3	6	4	4	2	1	2	T	9	7	2
HE minus	7	23	20r	8	1	3	4	6	4	4	2	1	0	T	9	7	2
HE minus	7	23-24	22f	1	1	3	5	6	4	4	2	1	1	T	9	7	2
HE minus	7	24	19r	1	2	3	0	5	4	4	2	2	0	T	9	7	2
HE minus	7	24	20f	9	2	3	1	5	4	4	2	2	1	T	9	7	2
HE minus	7	23-24	21r	17	2	3	2	5	4	4	2	2	2	T	9	7	2
HE minus	7	24	21f	13	2	3	3	6	4	4	2	3	2	T	9	7	2
HE minus	7	24	19f	4	2	3	4	6	4	4	2	3	0	T	9	7	2
HE minus	7	23-24	20r	8	2	3	5	6	4	4	2	3	1	T	9	7	2
HE minus	7	26	19r	1	4	3	0	5	4	4	2	4	0	T	9	7	2
HE minus	7	26	20f	9	4	3	1	5	4	4	2	4	1	T	9	7	2
HE minus	7	25-26	22r	17	4	3	2	5	4	4	2	4	2	T	9	7	2
HE minus	7	26	19f	4	4	3	3	6	4	4	2	5	2	T	9	7	2
HE minus	7	26	20r	8	4	3	4	6	4	4	2	5	0	T	9	7	2
HE minus	7	25-26	22f	13	4	3	5	6	4	4	2	5	1	T	9	7	2
HE minus	7	25	19r	17	3	3	0	5	4	4	2	6	0	T	9	7	2
HE minus	7	25	20f	9	3	3	1	5	4	4	2	6	1	T	9	7	2
HE minus	7	25-26	21r	1	3	3	2	5	4	4	2	6	2	T	9	7	2
HE minus	7	25	21f	4	3	3	3	6	4	4	2	7	2	T	9	7	2
HE minus	7	25	19f	13	3	3	4	6	4	4	2	7	0	T	9	7	2
HE minus	7	25-26	20r	8	3	3	5	6	4	4	2	7	1	T	9	7	2
HE minus	7	23-24	28f	15	1	2	0	3	4	4	2	0	0	B	9	7	2
HE minus	7	23-24	28m	2	1	2	1	3	4	4	2	0	1	B	9	7	2
HE minus	7	23-24	29f	6	1	2	2	3	4	4	2	0	2	B	9	7	2
HE minus	7	23-24	28r	5	1	2	3	4	4	4	2	1	2	B	9	7	2
HE minus	7	23-24	24r	14	1	2	4	4	4	4	2	1	0	B	9	7	2
HE minus	7	23-24	24f	18	1	2	5	4	4	4	2	1	1	B	9	7	2
HE minus	7	25-26	28f	6	4	2	0	3	4	4	2	2	0	B	9	7	2
HE minus	7	25-26	28m	18	4	2	1	3	4	4	2	2	1	B	9	7	2
HE minus	7	25-26	29f	15	4	2	2	3	4	4	2	2	2	B	9	7	2
HE minus	7	25-26	28r	14	4	2	3	4	4	4	2	3	2	B	9	7	2
HE minus	7	25-26	24r	5	4	2	4	4	4	4	2	3	0	B	9	7	2
HE minus	7	25-26	24f	2	4	2	5	4	4	4	2	3	1	B	9	7	2
HE minus	7	23-24	27f	15	2	2	0	3	4	4	2	4	0	B	9	7	2
HE minus	7	23-24	27m	18	2	2	1	3	4	4	2	4	1	B	9	7	2
HE minus	7	23-24	29m	6	2	2	2	3	4	4	2	4	2	B	9	7	2
HE minus	7	23-24	23f	2	2	2	3	4	4	4	2	5	2	B	9	7	2
HE minus	7	23-24	27r	14	2	2	4	4	4	4	2	5	0	B	9	7	2
HE minus	7	23-24	23r	5	2	2	5	4	4	4	2	5	1	B	9	7	2
HE minus	7	25-26	27f	6	3	2	0	3	4	4	2	6	0	B	9	7	2
HE minus	7	25-26	27m	2	3	2	1	3	4	4	2	6	1	B	9	7	2
HE minus	7	25-26	29m	15	3	2	2	3	4	4	2	6	2	B	9	7	2
HE minus	7	25-26	23f	18	3	2	3	4	4	4	2	7	2	B	9	7	2
HE minus	7	25-26	27r	5	3	2	4	4	4	4	2	7	0	B	9	7	2
HE minus	7	25-26	23r	14	3	2	5	4	4	4	2	7	1	B	9	7	2

Det	W	Phi	tower	pixel	RM	Card	QIE	Qfib	Crate	Hslot	HTR	Hfib	Hch	FPGA	Dslot	DCC	Dch
HB plus	7	23	9	9	4	3	3	6	4	5	3	0	2	T	9	7	3
HB plus	7	23	5	14	4	3	4	6	4	5	3	0	0	T	9	7	3
HB plus	7	23	1	17	4	3	5	6	4	5	3	0	1	T	9	7	3
HB plus	7	23	10	11	4	2	0	3	4	5	3	1	0	T	9	7	3
HB plus	7	23	6	15	4	2	1	3	4	5	3	1	1	T	9	7	3
HB plus	7	23	2	18	4	2	2	3	4	5	3	1	2	T	9	7	3
HB plus	7	24	9	8	3	3	3	6	4	5	3	2	2	T	9	7	3
HB plus	7	24	5	4	3	3	4	6	4	5	3	2	0	T	9	7	3
HB plus	7	24	1	2	3	3	5	6	4	5	3	2	1	T	9	7	3
HB plus	7	24	10	16	3	2	0	3	4	5	3	3	0	T	9	7	3
HB plus	7	24	6	12	3	2	1	3	4	5	3	3	1	T	9	7	3
HB plus	7	24	2	7	3	2	2	3	4	5	3	3	2	T	9	7	3
HB plus	7	25	9	12	2	3	3	6	4	5	3	4	2	T	9	7	3
HB plus	7	25	5	16	2	3	4	6	4	5	3	4	0	T	9	7	3
HB plus	7	25	1	18	2	3	5	6	4	5	3	4	1	T	9	7	3
HB plus	7	25	10	4	2	2	0	3	4	5	3	5	0	T	9	7	3
HB plus	7	25	6	8	2	2	1	3	4	5	3	5	1	T	9	7	3
HB plus	7	25	2	13	2	2	2	3	4	5	3	5	2	T	9	7	3
HB plus	7	26	9	11	1	3	3	6	4	5	3	6	2	T	9	7	3
HB plus	7	26	5	6	1	3	4	6	4	5	3	6	0	T	9	7	3
HB plus	7	26	1	3	1	3	5	6	4	5	3	6	1	T	9	7	3
HB plus	7	26	10	9	1	2	0	3	4	5	3	7	0	T	9	7	3
HB plus	7	26	6	5	1	2	1	3	4	5	3	7	1	T	9	7	3
HB plus	7	26	2	2	1	2	2	3	4	5	3	7	2	T	9	7	3
HB plus	7	23	12	4	4	2	3	4	4	5	3	0	2	B	9	7	3
HB plus	7	23	8	8	4	2	4	4	4	5	3	0	0	B	9	7	3
HB plus	7	23	4	13	4	2	5	4	4	5	3	0	1	B	9	7	3
HB plus	7	23	11	12	4	3	0	5	4	5	3	1	0	B	9	7	3
HB plus	7	23	7	16	4	3	1	5	4	5	3	1	1	B	9	7	3
HB plus	7	23	3	19	4	3	2	5	4	5	3	1	2	B	9	7	3
HB plus	7	24	12	14	3	2	3	4	4	5	3	2	2	B	9	7	3
HB plus	7	24	8	9	3	2	4	4	4	5	3	2	0	B	9	7	3
HB plus	7	24	4	5	3	2	5	4	4	5	3	2	1	B	9	7	3
HB plus	7	24	11	15	3	3	0	5	4	5	3	3	0	B	9	7	3
HB plus	7	24	7	11	3	3	1	5	4	5	3	3	1	B	9	7	3
HB plus	7	24	3	6	3	3	2	5	4	5	3	3	2	B	9	7	3
HB plus	7	25	12	6	2	2	3	4	4	5	3	4	2	B	9	7	3
HB plus	7	25	8	11	2	2	4	4	4	5	3	4	0	B	9	7	3
HB plus	7	25	4	15	2	2	5	4	4	5	3	4	1	B	9	7	3
HB plus	7	25	11	5	2	3	0	5	4	5	3	5	0	B	9	7	3
HB plus	7	25	7	9	2	3	1	5	4	5	3	5	1	B	9	7	3
HB plus	7	25	3	14	2	3	2	5	4	5	3	5	2	B	9	7	3
HB plus	7	26	12	16	1	2	3	4	4	5	3	6	2	B	9	7	3
HB plus	7	26	8	12	1	2	4	4	4	5	3	6	0	B	9	7	3
HB plus	7	26	4	7	1	2	5	4	4	5	3	6	1	B	9	7	3
HB plus	7	26	11	8	1	3	0	5	4	5	3	7	0	B	9	7	3
HB plus	7	26	7	4	1	3	1	5	4	5	3	7	1	B	9	7	3
HB plus	7	26	3	1	1	3	2	5	4	5	3	7	2	B	9	7	3

Det	W	Phi	tower	pixel	RM	Card	QIE	Qfib	Crate	Hslot	HTR	Hfib	Hch	FPGA	Dslot	DCC	Dch
HB plus	7	23	16r	2	4	1	0	1	4	6	4	0	0	T	9	7	4
HB plus	7	23	15r	7	4	1	1	1	4	6	4	0	1	T	9	7	4
HB plus	7	23	14	6	4	1	2	1	4	6	4	0	2	T	9	7	4
HB plus	7	23	16f	3	4	1	3	2	4	6	4	1	2	T	9	7	4
HB plus	7	23	15f	1	4	1	4	2	4	6	4	1	0	T	9	7	4
HB plus	7	23	13	5	4	1	5	2	4	6	4	1	1	T	9	7	4
HB plus	7	24	16r	3	3	1	0	1	4	6	4	2	0	T	9	7	4
HB plus	7	24	15r	17	3	1	1	1	4	6	4	2	1	T	9	7	4
HB plus	7	24	14	19	3	1	2	1	4	6	4	2	2	T	9	7	4
HB plus	7	24	16f	1	3	1	3	2	4	6	4	3	2	T	9	7	4
HB plus	7	24	15f	18	3	1	4	2	4	6	4	3	0	T	9	7	4
HB plus	7	24	13	13	3	1	5	2	4	6	4	3	1	T	9	7	4
HE plus	7	23	17	12	4	1	0	1	4	6	4	4	0	T	9	7	4
HE plus	7	23	16	7	4	1	1	1	4	6	4	4	1	T	9	7	4
HE plus	7	23-24	26f	16	4	1	2	1	4	6	4	4	2	T	9	7	4
HE plus	7	23-24	26r	19	4	1	3	2	4	6	4	5	2	T	9	7	4
HE plus	7	23	18f	11	4	1	4	2	4	6	4	5	0	T	9	7	4
HE plus	7	23	18r	3	4	1	5	2	4	6	4	5	1	T	9	7	4
HE plus	7	24	17	12	3	1	0	1	4	6	4	6	0	T	9	7	4
HE plus	7	24	16	16	3	1	1	1	4	6	4	6	1	T	9	7	4
HE plus	7	23-24	25f	7	3	1	2	1	4	6	4	6	2	T	9	7	4
HE plus	7	23-24	25r	3	3	1	3	2	4	6	4	7	2	T	9	7	4
HE plus	7	24	18f	11	3	1	4	2	4	6	4	7	0	T	9	7	4
HE plus	7	24	18r	19	3	1	5	2	4	6	4	7	1	T	9	7	4
HB plus	7	25	16r	17	2	1	0	1	4	6	4	0	0	B	9	7	4
HB plus	7	25	15r	3	2	1	1	1	4	6	4	0	1	B	9	7	4
HB plus	7	25	14	1	2	1	2	1	4	6	4	0	2	B	9	7	4
HB plus	7	25	16f	19	2	1	3	2	4	6	4	1	2	B	9	7	4
HB plus	7	25	15f	2	2	1	4	2	4	6	4	1	0	B	9	7	4
HB plus	7	25	13	7	2	1	5	2	4	6	4	1	1	B	9	7	4
HB plus	7	26	16r	18	1	1	0	1	4	6	4	2	0	B	9	7	4
HB plus	7	26	15r	13	1	1	1	1	4	6	4	2	1	B	9	7	4
HB plus	7	26	14	14	1	1	2	1	4	6	4	2	2	B	9	7	4
HB plus	7	26	16f	17	1	1	3	2	4	6	4	3	2	B	9	7	4
HB plus	7	26	15f	19	1	1	4	2	4	6	4	3	0	B	9	7	4
HB plus	7	26	13	15	1	1	5	2	4	6	4	3	1	B	9	7	4
HE plus	7	25	17	12	2	1	0	1	4	6	4	4	0	B	9	7	4
HE plus	7	25	16	7	2	1	1	1	4	6	4	4	1	B	9	7	4
HE plus	7	25-26	25f	16	2	1	2	1	4	6	4	4	2	B	9	7	4
HE plus	7	25-26	25r	19	2	1	3	2	4	6	4	5	2	B	9	7	4
HE plus	7	25	18f	11	2	1	4	2	4	6	4	5	0	B	9	7	4
HE plus	7	25	18r	3	2	1	5	2	4	6	4	5	1	B	9	7	4
HE plus	7	26	17	12	1	1	0	1	3	6	4	6	0	B	9	7	4
HE plus	7	26	16	16	1	1	1	1	3	6	4	6	1	B	9	7	4
HE plus	7	25-26	26f	7	1	1	2	1	3	6	4	6	2	B	9	7	4
HE plus	7	25-26	26r	3	1	1	3	2	3	6	4	7	2	B	9	7	4
HE plus	7	26	18f	11	1	1	4	2	3	6	4	7	0	B	9	7	4
HE plus	7	26	18r	19	1	1	5	2	3	6	4	7	1	B	9	7	4

Det	W	Phi	tower	pixel	RM	Card	QIE	Qfib	Crate	Hslot	HTR	Hfib	Hch	FPGA	Dslot	DCC	Dch
HE plus	7	23	19r	1	4	3	0	5	4	7	5	0	0	T	9	7	5
HE plus	7	23	20f	9	4	3	1	5	4	7	5	0	1	T	9	7	5
HE plus	7	23-24	22r	17	4	3	2	5	4	7	5	0	2	T	9	7	5
HE plus	7	23	19f	4	4	3	3	6	4	7	5	1	2	T	9	7	5
HE plus	7	23	20r	8	4	3	4	6	4	7	5	1	0	T	9	7	5
HE plus	7	23-24	22f	13	4	3	5	6	4	7	5	1	1	T	9	7	5
HE plus	7	24	19r	17	3	3	0	5	4	7	5	2	0	T	9	7	5
HE plus	7	24	20f	9	3	3	1	5	4	7	5	2	1	T	9	7	5
HE plus	7	23-24	21r	1	3	3	2	5	4	7	5	2	2	T	9	7	5
HE plus	7	24	21f	4	3	3	3	6	4	7	5	3	2	T	9	7	5
HE plus	7	24	19f	13	3	3	4	6	4	7	5	3	0	T	9	7	5
HE plus	7	23-24	20r	8	3	3	5	6	4	7	5	3	1	T	9	7	5
HE plus	7	26	19r	17	1	3	0	5	3	7	5	4	0	T	9	7	5
HE plus	7	26	20f	9	1	3	1	5	3	7	5	4	1	T	9	7	5
HE plus	7	25-26	22r	4	1	3	2	5	3	7	5	4	2	T	9	7	5
HE plus	7	26	19f	13	1	3	3	6	3	7	5	5	2	T	9	7	5
HE plus	7	26	20r	8	1	3	4	6	3	7	5	5	0	T	9	7	5
HE plus	7	25-26	22f	1	1	3	5	6	3	7	5	5	1	T	9	7	5
HE plus	7	25	19r	1	2	3	0	5	4	7	5	6	0	T	9	7	5
HE plus	7	25	20f	9	2	3	1	5	4	7	5	6	1	T	9	7	5
HE plus	7	25-26	21r	17	2	3	2	5	4	7	5	6	2	T	9	7	5
HE plus	7	25	21f	13	2	3	3	6	4	7	5	7	2	T	9	7	5
HE plus	7	25	19f	4	2	3	4	6	4	7	5	7	0	T	9	7	5
HE plus	7	25-26	20r	8	2	3	5	6	4	7	5	7	1	T	9	7	5
HE plus	7	23-24	28f	6	4	2	0	3	4	7	5	0	0	B	9	7	5
HE plus	7	23-24	28m	18	4	2	1	3	4	7	5	0	1	B	9	7	5
HE plus	7	23-24	29f	15	4	2	2	3	4	7	5	0	2	B	9	7	5
HE plus	7	23-24	28r	14	4	2	3	4	4	7	5	1	2	B	9	7	5
HE plus	7	23-24	24r	5	4	2	4	4	4	7	5	1	0	B	9	7	5
HE plus	7	23-24	24f	2	4	2	5	4	4	7	5	1	1	B	9	7	5
HE plus	7	25-26	28f	15	1	2	0	3	3	7	5	2	0	B	9	7	5
HE plus	7	25-26	28m	2	1	2	1	3	3	7	5	2	1	B	9	7	5
HE plus	7	25-26	29f	6	1	2	2	3	3	7	5	2	2	B	9	7	5
HE plus	7	25-26	28r	5	1	2	3	4	3	7	5	3	2	B	9	7	5
HE plus	7	25-26	24r	14	1	2	4	4	3	7	5	3	0	B	9	7	5
HE plus	7	25-26	24f	18	1	2	5	4	3	7	5	3	1	B	9	7	5
HE plus	7	23-24	27f	6	3	2	0	3	4	7	5	4	0	B	9	7	5
HE plus	7	23-24	27m	2	3	2	1	3	4	7	5	4	1	B	9	7	5
HE plus	7	23-24	29m	15	3	2	2	3	4	7	5	4	2	B	9	7	5
HE plus	7	23-24	23f	18	3	2	3	4	4	7	5	5	2	B	9	7	5
HE plus	7	23-24	27r	5	3	2	4	4	4	7	5	5	0	B	9	7	5
HE plus	7	23-24	23r	14	3	2	5	4	4	7	5	5	1	B	9	7	5
HE plus	7	25-26	27f	15	2	2	0	3	4	7	5	6	0	B	9	7	5
HE plus	7	25-26	27m	18	2	2	1	3	4	7	5	6	1	B	9	7	5
HE plus	7	25-26	29m	6	2	2	2	3	4	7	5	6	2	B	9	7	5
HE plus	7	25-26	23f	2	2	2	3	4	4	7	5	7	2	B	9	7	5
HE plus	7	25-26	27r	14	2	2	4	4	4	7	5	7	0	B	9	7	5
HE plus	7	25-26	23r	5	2	2	5	4	4	7	5	7	1	B	9	7	5

Det	W	Phi	tower	pixel	RM	Card	QIE	Qfib	Crate	Hslot	HTR	Hfib	Hch	FPGA	Dslot	DCC	Dch
HB minus	8	27	9	11	1	3	3	6	4	13	6	0	2	T	19	8	6
HB minus	8	27	5	6	1	3	4	6	4	13	6	0	0	T	19	8	6
HB minus	8	27	1	3	1	3	5	6	4	13	6	0	1	T	19	8	6
HB minus	8	27	10	9	1	2	0	3	4	13	6	1	0	T	19	8	6
HB minus	8	27	6	5	1	2	1	3	4	13	6	1	1	T	19	8	6
HB minus	8	27	2	2	1	2	2	3	4	13	6	1	2	T	19	8	6
HB minus	8	28	9	12	2	3	3	6	4	13	6	2	2	T	19	8	6
HB minus	8	28	5	16	2	3	4	6	4	13	6	2	0	T	19	8	6
HB minus	8	28	1	18	2	3	5	6	4	13	6	2	1	T	19	8	6
HB minus	8	28	10	4	2	2	0	3	4	13	6	3	0	T	19	8	6
HB minus	8	28	6	8	2	2	1	3	4	13	6	3	1	T	19	8	6
HB minus	8	28	2	13	2	2	2	3	4	13	6	3	2	T	19	8	6
HB minus	8	29	9	8	3	3	3	6	4	13	6	4	2	T	19	8	6
HB minus	8	29	5	4	3	3	4	6	4	13	6	4	0	T	19	8	6
HB minus	8	29	1	2	3	3	5	6	4	13	6	4	1	T	19	8	6
HB minus	8	29	10	16	3	2	0	3	4	13	6	5	0	T	19	8	6
HB minus	8	29	6	12	3	2	1	3	4	13	6	5	1	T	19	8	6
HB minus	8	29	2	7	3	2	2	3	4	13	6	5	2	T	19	8	6
HB minus	8	30	9	9	4	3	3	6	4	13	6	6	2	T	19	8	6
HB minus	8	30	5	14	4	3	4	6	4	13	6	6	0	T	19	8	6
HB minus	8	30	1	17	4	3	5	6	4	13	6	6	1	T	19	8	6
HB minus	8	30	10	11	4	2	0	3	4	13	6	7	0	T	19	8	6
HB minus	8	30	6	15	4	2	1	3	4	13	6	7	1	T	19	8	6
HB minus	8	30	2	18	4	2	2	3	4	13	6	7	2	T	19	8	6
HB minus	8	27	12	16	1	2	3	4	4	13	6	0	2	B	19	8	6
HB minus	8	27	8	12	1	2	4	4	4	13	6	0	0	B	19	8	6
HB minus	8	27	4	7	1	2	5	4	4	13	6	0	1	B	19	8	6
HB minus	8	27	11	8	1	3	0	5	4	13	6	1	0	B	19	8	6
HB minus	8	27	7	4	1	3	1	5	4	13	6	1	1	B	19	8	6
HB minus	8	27	3	1	1	3	2	5	4	13	6	1	2	B	19	8	6
HB minus	8	28	12	6	2	2	3	4	4	13	6	2	2	B	19	8	6
HB minus	8	28	8	11	2	2	4	4	4	13	6	2	0	B	19	8	6
HB minus	8	28	4	15	2	2	5	4	4	13	6	2	1	B	19	8	6
HB minus	8	28	11	5	2	3	0	5	4	13	6	3	0	B	19	8	6
HB minus	8	28	7	9	2	3	1	5	4	13	6	3	1	B	19	8	6
HB minus	8	28	3	14	2	3	2	5	4	13	6	3	2	B	19	8	6
HB minus	8	29	12	14	3	2	3	4	4	13	6	4	2	B	19	8	6
HB minus	8	29	8	9	3	2	4	4	4	13	6	4	0	B	19	8	6
HB minus	8	29	4	5	3	2	5	4	4	13	6	4	1	B	19	8	6
HB minus	8	29	11	15	3	3	0	5	4	13	6	5	0	B	19	8	6
HB minus	8	29	7	11	3	3	1	5	4	13	6	5	1	B	19	8	6
HB minus	8	29	3	6	3	3	2	5	4	13	6	5	2	B	19	8	6
HB minus	8	30	12	4	4	2	3	4	4	13	6	6	2	B	19	8	6
HB minus	8	30	8	8	4	2	4	4	4	13	6	6	0	B	19	8	6
HB minus	8	30	4	13	4	2	5	4	4	13	6	6	1	B	19	8	6
HB minus	8	30	11	12	4	3	0	5	4	13	6	7	0	B	19	8	6
HB minus	8	30	7	16	4	3	1	5	4	13	6	7	1	B	19	8	6
HB minus	8	30	3	19	4	3	2	5	4	13	6	7	2	B	19	8	6

Det	W	Phi	tower	pixel	RM	Card	QIE	Qfib	Crate	Hslot	HTR	Hfib	Hch	FPGA	Dslot	DCC	Dch
HB minus	8	27	16r	18	1	1	0	1	4	14	7	0	0	T	19	8	7
HB minus	8	27	15r	13	1	1	1	1	4	14	7	0	1	T	19	8	7
HB minus	8	27	14	14	1	1	2	1	4	14	7	0	2	T	19	8	7
HB minus	8	27	16f	17	1	1	3	2	4	14	7	1	2	T	19	8	7
HB minus	8	27	15f	19	1	1	4	2	4	14	7	1	0	T	19	8	7
HB minus	8	27	13	15	1	1	5	2	4	14	7	1	1	T	19	8	7
HB minus	8	28	16r	17	2	1	0	1	4	14	7	2	0	T	19	8	7
HB minus	8	28	15r	3	2	1	1	1	4	14	7	2	1	T	19	8	7
HB minus	8	28	14	1	2	1	2	1	4	14	7	2	2	T	19	8	7
HB minus	8	28	16f	19	2	1	3	2	4	14	7	3	2	T	19	8	7
HB minus	8	28	15f	2	2	1	4	2	4	14	7	3	0	T	19	8	7
HB minus	8	28	13	7	2	1	5	2	4	14	7	3	1	T	19	8	7
HE minus	8	27	17	12	1	1	0	1	4	14	7	4	0	T	19	8	7
HE minus	8	27	16	16	1	1	1	1	4	14	7	4	1	T	19	8	7
HE minus	8	27-28	26f	7	1	1	2	1	4	14	7	4	2	T	19	8	7
HE minus	8	27-28	26r	3	1	1	3	2	4	14	7	5	2	T	19	8	7
HE minus	8	27	18f	11	1	1	4	2	4	14	7	5	0	T	19	8	7
HE minus	8	27	18r	19	1	1	5	2	4	14	7	5	1	T	19	8	7
HE minus	8	28	17	12	2	1	0	1	4	14	7	6	0	T	19	8	7
HE minus	8	28	16	7	2	1	1	1	4	14	7	6	1	T	19	8	7
HE minus	8	27-28	25f	16	2	1	2	1	4	14	7	6	2	T	19	8	7
HE minus	8	27-28	25r	19	2	1	3	2	4	14	7	7	2	T	19	8	7
HE minus	8	28	18f	11	2	1	4	2	4	14	7	7	0	T	19	8	7
HE minus	8	28	18r	3	2	1	5	2	4	14	7	7	1	T	19	8	7
HB minus	8	29	16r	3	3	1	0	1	4	14	7	0	0	B	19	8	7
HB minus	8	29	15r	17	3	1	1	1	4	14	7	0	1	B	19	8	7
HB minus	8	29	14	19	3	1	2	1	4	14	7	0	2	B	19	8	7
HB minus	8	29	16f	1	3	1	3	2	4	14	7	1	2	B	19	8	7
HB minus	8	29	15f	18	3	1	4	2	4	14	7	1	0	B	19	8	7
HB minus	8	29	13	13	3	1	5	2	4	14	7	1	1	B	19	8	7
HB minus	8	30	16r	2	4	1	0	1	4	14	7	2	0	B	19	8	7
HB minus	8	30	15r	7	4	1	1	1	4	14	7	2	1	B	19	8	7
HB minus	8	30	14	6	4	1	2	1	4	14	7	2	2	B	19	8	7
HB minus	8	30	16f	3	4	1	3	2	4	14	7	3	2	B	19	8	7
HB minus	8	30	15f	1	4	1	4	2	4	14	7	3	0	B	19	8	7
HB minus	8	30	13	5	4	1	5	2	4	14	7	3	1	B	19	8	7
HE minus	8	29	17	12	3	1	0	1	4	14	7	4	0	B	19	8	7
HE minus	8	29	16	16	3	1	1	1	4	14	7	4	1	B	19	8	7
HE minus	8	29-30	25f	7	3	1	2	1	4	14	7	4	2	B	19	8	7
HE minus	8	29-30	25r	3	3	1	3	2	4	14	7	5	2	B	19	8	7
HE minus	8	29	18f	11	3	1	4	2	4	14	7	5	0	B	19	8	7
HE minus	8	29	18r	19	3	1	5	2	4	14	7	5	1	B	19	8	7
HE minus	8	30	17	12	4	1	0	1	4	14	7	6	0	B	19	8	7
HE minus	8	30	16	7	4	1	1	1	4	14	7	6	1	B	19	8	7
HE minus	8	29-30	26f	16	4	1	2	1	4	14	7	6	2	B	19	8	7
HE minus	8	29-30	26r	19	4	1	3	2	4	14	7	7	2	B	19	8	7
HE minus	8	30	18f	11	4	1	4	2	4	14	7	7	0	B	19	8	7
HE minus	8	30	18r	3	4	1	5	2	4	14	7	7	1	B	19	8	7

Det	W	Phi	tower	pixel	RM	Card	QIE	Qfib	Crate	Hslot	HTR	Hfib	Hch	FPGA	Dslot	DCC	Dch
HE minus	8	27	19r	17	1	3	0	5	4	15	8	0	0	T	19	8	8
HE minus	8	27	20f	9	1	3	1	5	4	15	8	0	1	T	19	8	8
HE minus	8	27-28	22r	4	1	3	2	5	4	15	8	0	2	T	19	8	8
HE minus	8	27-28	19f	13	1	3	3	6	4	15	8	1	2	T	19	8	8
HE minus	8	27	20r	8	1	3	4	6	4	15	8	1	0	T	19	8	8
HE minus	8	27-28	22f	1	1	3	5	6	4	15	8	1	1	T	19	8	8
HE minus	8	28	19r	1	2	3	0	5	4	15	8	2	0	T	19	8	8
HE minus	8	28	20f	9	2	3	1	5	4	15	8	2	1	T	19	8	8
HE minus	8	27-28	21r	17	2	3	2	5	4	15	8	2	2	T	19	8	8
HE minus	8	27-28	21f	13	2	3	3	6	4	15	8	3	2	T	19	8	8
HE minus	8	28	19f	4	2	3	4	6	4	15	8	3	0	T	19	8	8
HE minus	8	19-29	20r	8	2	3	5	6	4	15	8	3	1	T	19	8	8
HE minus	8	30	19r	1	4	3	0	5	4	15	8	4	0	T	19	8	8
HE minus	8	30	20f	9	4	3	1	5	4	15	8	4	1	T	19	8	8
HE minus	8	29-30	22r	17	4	3	2	5	4	15	8	4	2	T	19	8	8
HE minus	8	29-30	19f	4	4	3	3	6	4	15	8	5	2	T	19	8	8
HE minus	8	30	20r	8	4	3	4	6	4	15	8	5	0	T	19	8	8
HE minus	8	29-30	22f	13	4	3	5	6	4	15	8	5	1	T	19	8	8
HE minus	8	29	19r	17	3	3	0	5	4	15	8	6	0	T	19	8	8
HE minus	8	29	20f	9	3	3	1	5	4	15	8	6	1	T	19	8	8
HE minus	8	29-30	21r	1	3	3	2	5	4	15	8	6	2	T	19	8	8
HE minus	8	29-30	21f	4	3	3	3	6	4	15	8	7	2	T	19	8	8
HE minus	8	29	19f	13	3	3	4	6	4	15	8	7	0	T	19	8	8
HE minus	8	29-30	20r	8	3	3	5	6	4	15	8	7	1	T	19	8	8
HE minus	8	27-28	28f	15	1	2	0	3	4	15	8	0	0	B	19	8	8
HE minus	8	27-28	28m	2	1	2	1	3	4	15	8	0	1	B	19	8	8
HE minus	8	27-28	29f	6	1	2	2	3	4	15	8	0	2	B	19	8	8
HE minus	8	27-28	28r	5	1	2	3	4	4	15	8	1	2	B	19	8	8
HE minus	8	27-28	24r	14	1	2	4	4	4	15	8	1	0	B	19	8	8
HE minus	8	27-28	24f	18	1	2	5	4	4	15	8	1	1	B	19	8	8
HE minus	8	29-30	28f	6	4	2	0	3	4	15	8	2	0	B	19	8	8
HE minus	8	29-30	28m	18	4	2	1	3	4	15	8	2	1	B	19	8	8
HE minus	8	29-30	29f	15	4	2	2	3	4	15	8	2	2	B	19	8	8
HE minus	8	29-30	28r	14	4	2	3	4	4	15	8	3	2	B	19	8	8
HE minus	8	29-30	24r	5	4	2	4	4	4	15	8	3	0	B	19	8	8
HE minus	8	29-30	24f	2	4	2	5	4	4	15	8	3	1	B	19	8	8
HE minus	8	27-28	27f	15	2	2	0	3	4	15	8	4	0	B	19	8	8
HE minus	8	27-28	27m	18	2	2	1	3	4	15	8	4	1	B	19	8	8
HE minus	8	27-28	29m	6	2	2	2	3	4	15	8	4	2	B	19	8	8
HE minus	8	27-28	23f	2	2	2	3	4	4	15	8	5	2	B	19	8	8
HE minus	8	27-28	27r	14	2	2	4	4	4	15	8	5	0	B	19	8	8
HE minus	8	27-28	23r	5	2	2	5	4	4	15	8	5	1	B	19	8	8
HE minus	8	29-30	27f	6	3	2	0	3	4	15	8	6	0	B	19	8	8
HE minus	8	29-30	27m	2	3	2	1	3	4	15	8	6	1	B	19	8	8
HE minus	8	29-30	29m	15	3	2	2	3	4	15	8	6	2	B	19	8	8
HE minus	8	29-30	23f	18	3	2	3	4	4	15	8	7	2	B	19	8	8
HE minus	8	29-30	27r	5	3	2	4	4	4	15	8	7	0	B	19	8	8
HE minus	8	29-30	23r	14	3	2	5	4	4	15	8	7	1	B	19	8	8

Det	W	Phi	tower	pixel	RM	Card	QIE	Qfib	Crate	Hslot	HTR	Hfib	Hch	FPGA	Dslot	DCC	Dch
HB plus	8	27	9	9	4	3	3	6	4	16	9	0	2	T	19	8	9
HB plus	8	27	5	14	4	3	4	6	4	16	9	0	0	T	19	8	9
HB plus	8	27	1	17	4	3	5	6	4	16	9	0	1	T	19	8	9
HB plus	8	27	10	11	4	2	0	3	4	16	9	1	0	T	19	8	9
HB plus	8	27	6	15	4	2	1	3	4	16	9	1	1	T	19	8	9
HB plus	8	27	2	18	4	2	2	3	4	16	9	1	2	T	19	8	9
HB plus	8	28	9	8	3	3	3	6	4	16	9	2	2	T	19	8	9
HB plus	8	28	5	4	3	3	4	6	4	16	9	2	0	T	19	8	9
HB plus	8	28	1	2	3	3	5	6	4	16	9	2	1	T	19	8	9
HB plus	8	28	10	16	3	2	0	3	4	16	9	3	0	T	19	8	9
HB plus	8	28	6	12	3	2	1	3	4	16	9	3	1	T	19	8	9
HB plus	8	28	2	7	3	2	2	3	4	16	9	3	2	T	19	8	9
HB plus	8	29	9	12	2	3	3	6	4	16	9	4	2	T	19	8	9
HB plus	8	29	5	16	2	3	4	6	4	16	9	4	0	T	19	8	9
HB plus	8	29	1	18	2	3	5	6	4	16	9	4	1	T	19	8	9
HB plus	8	29	10	4	2	2	0	3	4	16	9	5	0	T	19	8	9
HB plus	8	29	6	8	2	2	1	3	4	16	9	5	1	T	19	8	9
HB plus	8	29	2	13	2	2	2	3	4	16	9	5	2	T	19	8	9
HB plus	8	30	9	11	1	3	3	6	4	16	9	6	2	T	19	8	9
HB plus	8	30	5	6	1	3	4	6	4	16	9	6	0	T	19	8	9
HB plus	8	30	1	3	1	3	5	6	4	16	9	6	1	T	19	8	9
HB plus	8	30	10	9	1	2	0	3	4	16	9	7	0	T	19	8	9
HB plus	8	30	6	5	1	2	1	3	4	16	9	7	1	T	19	8	9
HB plus	8	30	2	2	1	2	2	3	4	16	9	7	2	T	19	8	9
HB plus	8	27	12	4	4	2	3	4	4	16	9	0	2	B	19	8	9
HB plus	8	27	8	8	4	2	4	4	4	16	9	0	0	B	19	8	9
HB plus	8	27	4	13	4	2	5	4	4	16	9	0	1	B	19	8	9
HB plus	8	27	11	12	4	3	0	5	4	16	9	1	0	B	19	8	9
HB plus	8	27	7	16	4	3	1	5	4	16	9	1	1	B	19	8	9
HB plus	8	27	3	19	4	3	2	5	4	16	9	1	2	B	19	8	9
HB plus	8	28	12	14	3	2	3	4	4	16	9	2	2	B	19	8	9
HB plus	8	28	8	9	3	2	4	4	4	16	9	2	0	B	19	8	9
HB plus	8	28	4	5	3	2	5	4	4	16	9	2	1	B	19	8	9
HB plus	8	28	11	15	3	3	0	5	4	16	9	3	0	B	19	8	9
HB plus	8	28	7	11	3	3	1	5	4	16	9	3	1	B	19	8	9
HB plus	8	28	3	6	3	3	2	5	4	16	9	3	2	B	19	8	9
HB plus	8	29	12	6	2	2	3	4	4	16	9	4	2	B	19	8	9
HB plus	8	29	8	11	2	2	4	4	4	16	9	4	0	B	19	8	9
HB plus	8	29	4	15	2	2	5	4	4	16	9	4	1	B	19	8	9
HB plus	8	29	11	5	2	3	0	5	4	16	9	5	0	B	19	8	9
HB plus	8	29	7	9	2	3	1	5	4	16	9	5	1	B	19	8	9
HB plus	8	29	3	14	2	3	2	5	4	16	9	5	2	B	19	8	9
HB plus	8	30	12	16	1	2	3	4	4	16	9	6	2	B	19	8	9
HB plus	8	30	8	12	1	2	4	4	4	16	9	6	0	B	19	8	9
HB plus	8	30	4	7	1	2	5	4	4	16	9	6	1	B	19	8	9
HB plus	8	30	11	8	1	3	0	5	4	16	9	7	0	B	19	8	9
HB plus	8	30	7	4	1	3	1	5	4	16	9	7	1	B	19	8	9
HB plus	8	30	3	1	1	3	2	5	4	16	9	7	2	B	19	8	9

Det	W	Phi	tower	pixel	RM	Card	QIE	Qfib	Crate	Hslot	HTR	Hfib	Hch	FPGA	Dslot	DCC	Dch
HB plus	8	27	16r	2	4	1	0	1	4	17	10	0	0	T	19	8	10
HB plus	8	27	15r	7	4	1	1	1	4	17	10	0	1	T	19	8	10
HB plus	8	27	14	6	4	1	2	1	4	17	10	0	2	T	19	8	10
HB plus	8	27	16f	3	4	1	3	2	4	17	10	1	2	T	19	8	10
HB plus	8	27	15f	1	4	1	4	2	4	17	10	1	0	T	19	8	10
HB plus	8	27	13	5	4	1	5	2	4	17	10	1	1	T	19	8	10
HB plus	8	28	16r	3	3	1	0	1	4	17	10	2	0	T	19	8	10
HB plus	8	28	15r	17	3	1	1	1	4	17	10	2	1	T	19	8	10
HB plus	8	28	14	19	3	1	2	1	4	17	10	2	2	T	19	8	10
HB plus	8	28	16f	1	3	1	3	2	4	17	10	3	2	T	19	8	10
HB plus	8	28	15f	18	3	1	4	2	4	17	10	3	0	T	19	8	10
HB plus	8	28	13	13	3	1	5	2	4	17	10	3	1	T	19	8	10
HE plus	8	27	17	12	4	1	0	1	4	17	10	4	0	T	19	8	10
HE plus	8	27	16	7	4	1	1	1	4	17	10	4	1	T	19	8	10
HE plus	8	27-28	26f	16	4	1	2	1	4	17	10	4	2	T	19	8	10
HE plus	8	27-28	26r	19	4	1	3	2	4	17	10	5	2	T	19	8	10
HE plus	8	27	18f	11	4	1	4	2	4	17	10	5	0	T	19	8	10
HE plus	8	27	18r	3	4	1	5	2	4	17	10	5	1	T	19	8	10
HE plus	8	28	17	12	3	1	0	1	4	17	10	6	0	T	19	8	10
HE plus	8	28	16	16	3	1	1	1	4	17	10	6	1	T	19	8	10
HE plus	8	27-28	25f	7	3	1	2	1	4	17	10	6	2	T	19	8	10
HE plus	8	27-28	25r	3	3	1	3	2	4	17	10	7	2	T	19	8	10
HE plus	8	28	18f	11	3	1	4	2	4	17	10	7	0	T	19	8	10
HE plus	8	28	18r	19	3	1	5	2	4	17	10	7	1	T	19	8	10
HB plus	8	29	16r	17	2	1	0	1	4	17	10	0	0	B	19	8	10
HB plus	8	29	15r	3	2	1	1	1	4	17	10	0	1	B	19	8	10
HB plus	8	29	14	1	2	1	2	1	4	17	10	0	2	B	19	8	10
HB plus	8	29	16f	19	2	1	3	2	4	17	10	1	2	B	19	8	10
HB plus	8	29	15f	2	2	1	4	2	4	17	10	1	0	B	19	8	10
HB plus	8	29	13	7	2	1	5	2	4	17	10	1	1	B	19	8	10
HB plus	8	30	16r	18	1	1	0	1	4	17	10	2	0	B	19	8	10
HB plus	8	30	15r	13	1	1	1	1	4	17	10	2	1	B	19	8	10
HB plus	8	30	14	14	1	1	2	1	4	17	10	2	2	B	19	8	10
HB plus	8	30	16f	17	1	1	3	2	4	17	10	3	2	B	19	8	10
HB plus	8	30	15f	19	1	1	4	2	4	17	10	3	0	B	19	8	10
HB plus	8	30	13	15	1	1	5	2	4	17	10	3	1	B	19	8	10
HE plus	8	29	17	12	2	1	0	1	4	17	10	4	0	B	19	8	10
HE plus	8	29	16	7	2	1	1	1	4	17	10	4	1	B	19	8	10
HE plus	8	29-30	25f	16	2	1	2	1	4	17	10	4	2	B	19	8	10
HE plus	8	29-30	25r	19	2	1	3	2	4	17	10	5	2	B	19	8	10
HE plus	8	29	18f	11	2	1	4	2	4	17	10	5	0	B	19	8	10
HE plus	8	29	18r	3	2	1	5	2	4	17	10	5	1	B	19	8	10
HE plus	8	30	17	12	1	1	0	1	4	17	10	6	0	B	19	8	10
HE plus	8	30	16	16	1	1	1	1	4	17	10	6	1	B	19	8	10
HE plus	8	29-30	26f	7	1	1	2	1	4	17	10	6	2	B	19	8	10
HE plus	8	29-30	26r	3	1	1	3	2	4	17	10	7	2	B	19	8	10
HE plus	8	30	18f	11	1	1	4	2	4	17	10	7	0	B	19	8	10
HE plus	8	30	18r	19	1	1	5	2	4	17	10	7	1	B	19	8	10

Det	W	Phi	tower	pixel	RM	Card	QIE	Qfib	Crate	Hslot	HTR	Hfib	Hch	FPGA	Dslot	DCC	Dch
HE plus	8	27	19r	1	4	3	0	5	4	18	11	0	0	T	19	8	11
HE plus	8	27	20f	9	4	3	1	5	4	18	11	0	1	T	19	8	11
HE plus	8	27-28	22r	17	4	3	2	5	4	18	11	0	2	T	19	8	11
HE plus	8	27-28	19f	4	4	3	3	6	4	18	11	1	2	T	19	8	11
HE plus	8	27	20r	8	4	3	4	6	4	18	11	1	0	T	19	8	11
HE plus	8	27-28	22f	13	4	3	5	6	4	18	11	1	1	T	19	8	11
HE plus	8	28	19r	17	3	3	0	5	4	18	11	2	0	T	19	8	11
HE plus	8	28	20f	9	3	3	1	5	4	18	11	2	1	T	19	8	11
HE plus	8	27-28	21r	1	3	3	2	5	4	18	11	2	2	T	19	8	11
HE plus	8	27-28	21f	4	3	3	3	6	4	18	11	3	2	T	19	8	11
HE plus	8	28	19f	13	3	3	4	6	4	18	11	3	0	T	19	8	11
HE plus	8	19-29	20r	8	3	3	5	6	4	18	11	3	1	T	19	8	11
HE plus	8	30	19r	17	1	3	0	5	4	18	11	4	0	T	19	8	11
HE plus	8	30	20f	9	1	3	1	5	4	18	11	4	1	T	19	8	11
HE plus	8	29-30	22r	4	1	3	2	5	4	18	11	4	2	T	19	8	11
HE plus	8	29-30	19f	13	1	3	3	6	4	18	11	5	2	T	19	8	11
HE plus	8	30	20r	8	1	3	4	6	4	18	11	5	0	T	19	8	11
HE plus	8	29-30	22f	1	1	3	5	6	4	18	11	5	1	T	19	8	11
HE plus	8	29	19r	1	2	3	0	5	4	18	11	6	0	T	19	8	11
HE plus	8	29	20f	9	2	3	1	5	4	18	11	6	1	T	19	8	11
HE plus	8	29-30	21r	17	2	3	2	5	4	18	11	6	2	T	19	8	11
HE plus	8	29-30	21f	13	2	3	3	6	4	18	11	7	2	T	19	8	11
HE plus	8	29	19f	4	2	3	4	6	4	18	11	7	0	T	19	8	11
HE plus	8	29-30	20r	8	2	3	5	6	4	18	11	7	1	T	19	8	11
HE plus	8	27-28	28f	6	4	2	0	3	4	18	11	0	0	B	19	8	11
HE plus	8	27-28	28m	18	4	2	1	3	4	18	11	0	1	B	19	8	11
HE plus	8	27-28	29f	15	4	2	2	3	4	18	11	0	2	B	19	8	11
HE plus	8	27-28	28r	14	4	2	3	4	4	18	11	1	2	B	19	8	11
HE plus	8	27-28	24r	5	4	2	4	4	4	18	11	1	0	B	19	8	11
HE plus	8	27-28	24f	2	4	2	5	4	4	18	11	1	1	B	19	8	11
HE plus	8	29-30	28f	15	1	2	0	3	4	18	11	2	0	B	19	8	11
HE plus	8	29-30	28m	2	1	2	1	3	4	18	11	2	1	B	19	8	11
HE plus	8	29-30	29f	6	1	2	2	3	4	18	11	2	2	B	19	8	11
HE plus	8	29-30	28r	5	1	2	3	4	4	18	11	3	2	B	19	8	11
HE plus	8	29-30	24r	14	1	2	4	4	4	18	11	3	0	B	19	8	11
HE plus	8	29-30	24f	18	1	2	5	4	4	18	11	3	1	B	19	8	11
HE plus	8	27-28	27f	6	3	2	0	3	4	18	11	4	0	B	19	8	11
HE plus	8	27-28	27m	2	3	2	1	3	4	18	11	4	1	B	19	8	11
HE plus	8	27-28	29m	15	3	2	2	3	4	18	11	4	2	B	19	8	11
HE plus	8	27-28	23f	18	3	2	3	4	4	18	11	5	2	B	19	8	11
HE plus	8	27-28	27r	5	3	2	4	4	4	18	11	5	0	B	19	8	11
HE plus	8	27-28	23r	14	3	2	5	4	4	18	11	5	1	B	19	8	11
HE plus	8	29-30	27f	15	2	2	0	3	4	18	11	6	0	B	19	8	11
HE plus	8	29-30	27m	18	2	2	1	3	4	18	11	6	1	B	19	8	11
HE plus	8	29-30	29m	6	2	2	2	3	4	18	11	6	2	B	19	8	11
HE plus	8	29-30	23f	2	2	2	3	4	4	18	11	7	2	B	19	8	11
HE plus	8	29-30	27r	14	2	2	4	4	4	18	11	7	0	B	19	8	11
HE plus	8	29-30	23r	5	2	2	5	4	4	18	11	7	1	B	19	8	11