

| Det | W | Phi | tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch |
|----------|----|-----|-------|-------|----|------|-----|------|-------|-------|-----|------|-----|------|-------|-----|-----|
| HB minus | 17 | 63 | 16r | 18 | 1 | 1 | 0 | 1 | 9 | 3 | 1 | 0 | 0 | T | 9 | 17 | 1 |
| HB minus | 17 | 63 | 15r | 13 | 1 | 1 | 1 | 1 | 9 | 3 | 1 | 0 | 1 | T | 9 | 17 | 1 |
| HB minus | 17 | 63 | 14 | 14 | 1 | 1 | 2 | 1 | 9 | 3 | 1 | 0 | 2 | T | 9 | 17 | 1 |
| HB minus | 17 | 63 | 16f | 17 | 1 | 1 | 3 | 2 | 9 | 3 | 1 | 1 | 2 | T | 9 | 17 | 1 |
| HB minus | 17 | 63 | 15f | 19 | 1 | 1 | 4 | 2 | 9 | 3 | 1 | 1 | 0 | T | 9 | 17 | 1 |
| HB minus | 17 | 63 | 13 | 15 | 1 | 1 | 5 | 2 | 9 | 3 | 1 | 1 | 1 | T | 9 | 17 | 1 |
| HB minus | 17 | 63 | 10 | 9 | 1 | 2 | 0 | 3 | 9 | 2 | 0 | 1 | 0 | T | 9 | 17 | 0 |
| HB minus | 17 | 63 | 6 | 5 | 1 | 2 | 1 | 3 | 9 | 2 | 0 | 1 | 1 | T | 9 | 17 | 0 |
| HB minus | 17 | 63 | 2 | 2 | 1 | 2 | 2 | 3 | 9 | 2 | 0 | 1 | 2 | T | 9 | 17 | 0 |
| HB minus | 17 | 63 | 12 | 16 | 1 | 2 | 3 | 4 | 9 | 2 | 0 | 0 | 2 | B | 9 | 17 | 0 |
| HB minus | 17 | 63 | 8 | 12 | 1 | 2 | 4 | 4 | 9 | 2 | 0 | 0 | 0 | B | 9 | 17 | 0 |
| HB minus | 17 | 63 | 4 | 7 | 1 | 2 | 5 | 4 | 9 | 2 | 0 | 0 | 1 | B | 9 | 17 | 0 |
| HB minus | 17 | 63 | 11 | 8 | 1 | 3 | 0 | 5 | 9 | 2 | 0 | 1 | 0 | B | 9 | 17 | 0 |
| HB minus | 17 | 63 | 7 | 4 | 1 | 3 | 1 | 5 | 9 | 2 | 0 | 1 | 1 | B | 9 | 17 | 0 |
| HB minus | 17 | 63 | 3 | 1 | 1 | 3 | 2 | 5 | 9 | 2 | 0 | 1 | 2 | B | 9 | 17 | 0 |
| HB minus | 17 | 63 | 9 | 11 | 1 | 3 | 3 | 6 | 9 | 2 | 0 | 0 | 2 | T | 9 | 17 | 0 |
| HB minus | 17 | 63 | 5 | 6 | 1 | 3 | 4 | 6 | 9 | 2 | 0 | 0 | 0 | T | 9 | 17 | 0 |
| HB minus | 17 | 63 | 1 | 3 | 1 | 3 | 5 | 6 | 9 | 2 | 0 | 0 | 1 | T | 9 | 17 | 0 |
| Det | W | Phi | tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch |
| HB minus | 17 | 64 | 16r | 17 | 2 | 1 | 0 | 1 | 9 | 3 | 1 | 2 | 0 | T | 9 | 17 | 1 |
| HB minus | 17 | 64 | 15r | 3 | 2 | 1 | 1 | 1 | 9 | 3 | 1 | 2 | 1 | T | 9 | 17 | 1 |
| HB minus | 17 | 64 | 14 | 1 | 2 | 1 | 2 | 1 | 9 | 3 | 1 | 2 | 2 | T | 9 | 17 | 1 |
| HB minus | 17 | 64 | 16f | 19 | 2 | 1 | 3 | 2 | 9 | 3 | 1 | 3 | 2 | T | 9 | 17 | 1 |
| HB minus | 17 | 64 | 15f | 2 | 2 | 1 | 4 | 2 | 9 | 3 | 1 | 3 | 0 | T | 9 | 17 | 1 |
| HB minus | 17 | 64 | 13 | 7 | 2 | 1 | 5 | 2 | 9 | 3 | 1 | 3 | 1 | T | 9 | 17 | 1 |
| HB minus | 17 | 64 | 10 | 4 | 2 | 2 | 0 | 3 | 9 | 2 | 0 | 3 | 0 | T | 9 | 17 | 0 |
| HB minus | 17 | 64 | 6 | 8 | 2 | 2 | 1 | 3 | 9 | 2 | 0 | 3 | 1 | T | 9 | 17 | 0 |
| HB minus | 17 | 64 | 2 | 13 | 2 | 2 | 2 | 3 | 9 | 2 | 0 | 3 | 2 | T | 9 | 17 | 0 |
| HB minus | 17 | 64 | 12 | 6 | 2 | 2 | 3 | 4 | 9 | 2 | 0 | 2 | 2 | B | 9 | 17 | 0 |
| HB minus | 17 | 64 | 8 | 11 | 2 | 2 | 4 | 4 | 9 | 2 | 0 | 2 | 0 | B | 9 | 17 | 0 |
| HB minus | 17 | 64 | 4 | 15 | 2 | 2 | 5 | 4 | 9 | 2 | 0 | 2 | 1 | B | 9 | 17 | 0 |
| HB minus | 17 | 64 | 11 | 5 | 2 | 3 | 0 | 5 | 9 | 2 | 0 | 3 | 0 | B | 9 | 17 | 0 |
| HB minus | 17 | 64 | 7 | 9 | 2 | 3 | 1 | 5 | 9 | 2 | 0 | 3 | 1 | B | 9 | 17 | 0 |
| HB minus | 17 | 64 | 3 | 14 | 2 | 3 | 2 | 5 | 9 | 2 | 0 | 3 | 2 | B | 9 | 17 | 0 |
| HB minus | 17 | 64 | 9 | 12 | 2 | 3 | 3 | 6 | 9 | 2 | 0 | 2 | 2 | T | 9 | 17 | 0 |
| HB minus | 17 | 64 | 5 | 16 | 2 | 3 | 4 | 6 | 9 | 2 | 0 | 2 | 0 | T | 9 | 17 | 0 |
| HB minus | 17 | 64 | 1 | 18 | 2 | 3 | 5 | 6 | 9 | 2 | 0 | 2 | 1 | T | 9 | 17 | 0 |
| Det | W | Phi | tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch |
| HB minus | 17 | 65 | 16r | 3 | 3 | 1 | 0 | 1 | 9 | 3 | 1 | 0 | 0 | B | 9 | 17 | 1 |
| HB minus | 17 | 65 | 15r | 17 | 3 | 1 | 1 | 1 | 9 | 3 | 1 | 0 | 1 | B | 9 | 17 | 1 |
| HB minus | 17 | 65 | 14 | 19 | 3 | 1 | 2 | 1 | 9 | 3 | 1 | 0 | 2 | B | 9 | 17 | 1 |
| HB minus | 17 | 65 | 16f | 1 | 3 | 1 | 3 | 2 | 9 | 3 | 1 | 1 | 2 | B | 9 | 17 | 1 |
| HB minus | 17 | 65 | 15f | 18 | 3 | 1 | 4 | 2 | 9 | 3 | 1 | 1 | 0 | B | 9 | 17 | 1 |
| HB minus | 17 | 65 | 13 | 13 | 3 | 1 | 5 | 2 | 9 | 3 | 1 | 1 | 1 | B | 9 | 17 | 1 |
| HB minus | 17 | 65 | 10 | 16 | 3 | 2 | 0 | 3 | 9 | 2 | 0 | 5 | 0 | T | 9 | 17 | 0 |
| HB minus | 17 | 65 | 6 | 12 | 3 | 2 | 1 | 3 | 9 | 2 | 0 | 5 | 1 | T | 9 | 17 | 0 |
| HB minus | 17 | 65 | 2 | 7 | 3 | 2 | 2 | 3 | 9 | 2 | 0 | 5 | 2 | T | 9 | 17 | 0 |
| HB minus | 17 | 65 | 12 | 14 | 3 | 2 | 3 | 4 | 9 | 2 | 0 | 4 | 2 | B | 9 | 17 | 0 |
| HB minus | 17 | 65 | 8 | 9 | 3 | 2 | 4 | 4 | 9 | 2 | 0 | 4 | 0 | B | 9 | 17 | 0 |

| | | | | | | | | | | | | | | | | | |
|----------|----|-----------------|-----|------|-----|------|-------|-------|-----|------|-----|------|-------|-----|-----|----|---|
| HB minus | 17 | 65 | 4 | 5 | 3 | 2 | 5 | 4 | 9 | 2 | 0 | 4 | 1 | B | 9 | 17 | 0 |
| HB minus | 17 | 65 | 11 | 15 | 3 | 3 | 0 | 5 | 9 | 2 | 0 | 5 | 0 | B | 9 | 17 | 0 |
| HB minus | 17 | 65 | 7 | 11 | 3 | 3 | 1 | 5 | 9 | 2 | 0 | 5 | 1 | B | 9 | 17 | 0 |
| HB minus | 17 | 65 | 3 | 6 | 3 | 3 | 2 | 5 | 9 | 2 | 0 | 5 | 2 | B | 9 | 17 | 0 |
| HB minus | 17 | 65 | 9 | 8 | 3 | 3 | 3 | 6 | 9 | 2 | 0 | 4 | 2 | T | 9 | 17 | 0 |
| HB minus | 17 | 65 | 5 | 4 | 3 | 3 | 4 | 6 | 9 | 2 | 0 | 4 | 0 | T | 9 | 17 | 0 |
| HB minus | 17 | 65 | 1 | 2 | 3 | 3 | 5 | 6 | 9 | 2 | 0 | 4 | 1 | T | 9 | 17 | 0 |
| Det | W | Phi tower pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | | |
| HB minus | 17 | 66 | 16r | 2 | 4 | 1 | 0 | 1 | 9 | 3 | 1 | 2 | 0 | B | 9 | 17 | 1 |
| HB minus | 17 | 66 | 15r | 7 | 4 | 1 | 1 | 1 | 9 | 3 | 1 | 2 | 1 | B | 9 | 17 | 1 |
| HB minus | 17 | 66 | 14 | 6 | 4 | 1 | 2 | 1 | 9 | 3 | 1 | 2 | 2 | B | 9 | 17 | 1 |
| HB minus | 17 | 66 | 16f | 3 | 4 | 1 | 3 | 2 | 9 | 3 | 1 | 3 | 2 | B | 9 | 17 | 1 |
| HB minus | 17 | 66 | 15f | 1 | 4 | 1 | 4 | 2 | 9 | 3 | 1 | 3 | 0 | B | 9 | 17 | 1 |
| HB minus | 17 | 66 | 13 | 5 | 4 | 1 | 5 | 2 | 9 | 3 | 1 | 3 | 1 | B | 9 | 17 | 1 |
| HB minus | 17 | 66 | 10 | 11 | 4 | 2 | 0 | 3 | 9 | 2 | 0 | 7 | 0 | T | 9 | 17 | 0 |
| HB minus | 17 | 66 | 6 | 15 | 4 | 2 | 1 | 3 | 9 | 2 | 0 | 7 | 1 | T | 9 | 17 | 0 |
| HB minus | 17 | 66 | 2 | 18 | 4 | 2 | 2 | 3 | 9 | 2 | 0 | 7 | 2 | T | 9 | 17 | 0 |
| HB minus | 17 | 66 | 12 | 4 | 4 | 2 | 3 | 4 | 9 | 2 | 0 | 6 | 2 | B | 9 | 17 | 0 |
| HB minus | 17 | 66 | 8 | 8 | 4 | 2 | 4 | 4 | 9 | 2 | 0 | 6 | 0 | B | 9 | 17 | 0 |
| HB minus | 17 | 66 | 4 | 13 | 4 | 2 | 5 | 4 | 9 | 2 | 0 | 6 | 1 | B | 9 | 17 | 0 |
| HB minus | 17 | 66 | 11 | 12 | 4 | 3 | 0 | 5 | 9 | 2 | 0 | 7 | 0 | B | 9 | 17 | 0 |
| HB minus | 17 | 66 | 7 | 16 | 4 | 3 | 1 | 5 | 9 | 2 | 0 | 7 | 1 | B | 9 | 17 | 0 |
| HB minus | 17 | 66 | 3 | 19 | 4 | 3 | 2 | 5 | 9 | 2 | 0 | 7 | 2 | B | 9 | 17 | 0 |
| HB minus | 17 | 66 | 9 | 9 | 4 | 3 | 3 | 6 | 9 | 2 | 0 | 6 | 2 | T | 9 | 17 | 0 |
| HB minus | 17 | 66 | 5 | 14 | 4 | 3 | 4 | 6 | 9 | 2 | 0 | 6 | 0 | T | 9 | 17 | 0 |
| HB minus | 17 | 66 | 1 | 17 | 4 | 3 | 5 | 6 | 9 | 2 | 0 | 6 | 1 | T | 9 | 17 | 0 |
| Det | W | Phi tower pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | | |
| HE minus | 17 | 63 | 17 | 12 | 1 | 1 | 0 | 1 | 9 | 3 | 1 | 4 | 0 | T | 9 | 17 | 1 |
| HE minus | 17 | 63 | 16 | 16 | 1 | 1 | 1 | 1 | 9 | 3 | 1 | 4 | 1 | T | 9 | 17 | 1 |
| HE minus | 17 | 63-64 | 26f | 7 | 1 | 1 | 2 | 1 | 9 | 3 | 1 | 4 | 2 | T | 9 | 17 | 1 |
| HE minus | 17 | 63-64 | 26r | 3 | 1 | 1 | 3 | 2 | 9 | 3 | 1 | 5 | 2 | T | 9 | 17 | 1 |
| HE minus | 17 | 63 | 18f | 11 | 1 | 1 | 4 | 2 | 9 | 3 | 1 | 5 | 0 | T | 9 | 17 | 1 |
| HE minus | 17 | 63 | 18r | 19 | 1 | 1 | 5 | 2 | 9 | 3 | 1 | 5 | 1 | T | 9 | 17 | 1 |
| HE minus | 17 | 63-64 | 28f | 15 | 1 | 2 | 0 | 3 | 9 | 4 | 2 | 0 | 0 | B | 9 | 17 | 2 |
| HE minus | 17 | 63-64 | 28m | 2 | 1 | 2 | 1 | 3 | 9 | 4 | 2 | 0 | 1 | B | 9 | 17 | 2 |
| HE minus | 17 | 63-64 | 29f | 6 | 1 | 2 | 2 | 3 | 9 | 4 | 2 | 0 | 2 | B | 9 | 17 | 2 |
| HE minus | 17 | 63-64 | 28r | 5 | 1 | 2 | 3 | 4 | 9 | 4 | 2 | 1 | 2 | B | 9 | 17 | 2 |
| HE minus | 17 | 63-64 | 24r | 14 | 1 | 2 | 4 | 4 | 9 | 4 | 2 | 1 | 0 | B | 9 | 17 | 2 |
| HE minus | 17 | 63-64 | 24f | 18 | 1 | 2 | 5 | 4 | 9 | 4 | 2 | 1 | 1 | B | 9 | 17 | 2 |
| HE minus | 17 | 63 | 19r | 17 | 1 | 3 | 0 | 5 | 9 | 4 | 2 | 0 | 0 | T | 9 | 17 | 2 |
| HE minus | 17 | 63 | 20f | 9 | 1 | 3 | 1 | 5 | 9 | 4 | 2 | 0 | 1 | T | 9 | 17 | 2 |
| HE minus | 17 | 63-64 | 22r | 4 | 1 | 3 | 2 | 5 | 9 | 4 | 2 | 0 | 2 | T | 9 | 17 | 2 |
| HE minus | 17 | 63 | 19f | 13 | 1 | 3 | 3 | 6 | 9 | 4 | 2 | 1 | 2 | T | 9 | 17 | 2 |
| HE minus | 17 | 63 | 20r | 8 | 1 | 3 | 4 | 6 | 9 | 4 | 2 | 1 | 0 | T | 9 | 17 | 2 |
| HE minus | 17 | 63-64 | 22f | 1 | 1 | 3 | 5 | 6 | 9 | 4 | 2 | 1 | 1 | T | 9 | 17 | 2 |
| Det | W | Phi tower pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | | |
| HE minus | 17 | 64 | 17 | 12 | 2 | 1 | 0 | 1 | 9 | 3 | 1 | 6 | 0 | T | 9 | 17 | 1 |
| HE minus | 17 | 64 | 16 | 7 | 2 | 1 | 1 | 1 | 9 | 3 | 1 | 6 | 1 | T | 9 | 17 | 1 |
| HE minus | 17 | 63-64 | 25f | 16 | 2 | 1 | 2 | 1 | 9 | 3 | 1 | 6 | 2 | T | 9 | 17 | 1 |
| HE minus | 17 | 63-64 | 25r | 19 | 2 | 1 | 3 | 2 | 9 | 3 | 1 | 7 | 2 | T | 9 | 17 | 1 |
| HE minus | 17 | 64 | 18f | 11 | 2 | 1 | 4 | 2 | 9 | 3 | 1 | 7 | 0 | T | 9 | 17 | 1 |

| | | | | | | | | | | | | | | | | | |
|----------|----|-----------|-------|----|------|-----|------|-------|-------|-----|------|-----|------|-------|-----|-----|---|
| HE minus | 17 | 64 | 18r | 3 | 2 | 1 | 5 | 2 | 9 | 3 | 1 | 7 | 1 | T | 9 | 17 | 1 |
| HE minus | 17 | 63-64 | 27f | 15 | 2 | 2 | 0 | 3 | 9 | 4 | 2 | 4 | 0 | B | 9 | 17 | 2 |
| HE minus | 17 | 63-64 | 27m | 18 | 2 | 2 | 1 | 3 | 9 | 4 | 2 | 4 | 1 | B | 9 | 17 | 2 |
| HE minus | 17 | 63-64 | 29m | 6 | 2 | 2 | 2 | 3 | 9 | 4 | 2 | 4 | 2 | B | 9 | 17 | 2 |
| HE minus | 17 | 63-64 | 23f | 2 | 2 | 2 | 3 | 4 | 9 | 4 | 2 | 5 | 2 | B | 9 | 17 | 2 |
| HE minus | 17 | 63-64 | 27r | 14 | 2 | 2 | 4 | 4 | 9 | 4 | 2 | 5 | 0 | B | 9 | 17 | 2 |
| HE minus | 17 | 63-64 | 23r | 5 | 2 | 2 | 5 | 4 | 9 | 4 | 2 | 5 | 1 | B | 9 | 17 | 2 |
| HE minus | 17 | 64 | 19r | 1 | 2 | 3 | 0 | 5 | 9 | 4 | 2 | 2 | 0 | T | 9 | 17 | 2 |
| HE minus | 17 | 64 | 20f | 9 | 2 | 3 | 1 | 5 | 9 | 4 | 2 | 2 | 1 | T | 9 | 17 | 2 |
| HE minus | 17 | 63-64 | 21r | 17 | 2 | 3 | 2 | 5 | 9 | 4 | 2 | 2 | 2 | T | 9 | 17 | 2 |
| HE minus | 17 | 64 | 21f | 13 | 2 | 3 | 3 | 6 | 9 | 4 | 2 | 3 | 2 | T | 9 | 17 | 2 |
| HE minus | 17 | 64 | 19f | 4 | 2 | 3 | 4 | 6 | 9 | 4 | 2 | 3 | 0 | T | 9 | 17 | 2 |
| HE minus | 17 | 63-64 | 20r | 8 | 2 | 3 | 5 | 6 | 9 | 4 | 2 | 3 | 1 | T | 9 | 17 | 2 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |
| HE minus | 17 | 65 | 17 | 12 | 3 | 1 | 0 | 1 | 9 | 3 | 1 | 4 | 0 | B | 9 | 17 | 1 |
| HE minus | 17 | 65 | 16 | 16 | 3 | 1 | 1 | 1 | 9 | 3 | 1 | 4 | 1 | B | 9 | 17 | 1 |
| HE minus | 17 | 65-66 | 25f | 7 | 3 | 1 | 2 | 1 | 9 | 3 | 1 | 4 | 2 | B | 9 | 17 | 1 |
| HE minus | 17 | 65-66 | 25r | 3 | 3 | 1 | 3 | 2 | 9 | 3 | 1 | 5 | 2 | B | 9 | 17 | 1 |
| HE minus | 17 | 65 | 18f | 11 | 3 | 1 | 4 | 2 | 9 | 3 | 1 | 5 | 0 | B | 9 | 17 | 1 |
| HE minus | 17 | 65 | 18r | 19 | 3 | 1 | 5 | 2 | 9 | 3 | 1 | 5 | 1 | B | 9 | 17 | 1 |
| HE minus | 17 | 65-66 | 27f | 6 | 3 | 2 | 0 | 3 | 9 | 4 | 2 | 6 | 0 | B | 9 | 17 | 2 |
| HE minus | 17 | 65-66 | 27m | 2 | 3 | 2 | 1 | 3 | 9 | 4 | 2 | 6 | 1 | B | 9 | 17 | 2 |
| HE minus | 17 | 65-66 | 29m | 15 | 3 | 2 | 2 | 3 | 9 | 4 | 2 | 6 | 2 | B | 9 | 17 | 2 |
| HE minus | 17 | 65-66 | 23f | 18 | 3 | 2 | 3 | 4 | 9 | 4 | 2 | 7 | 2 | B | 9 | 17 | 2 |
| HE minus | 17 | 65-66 | 27r | 5 | 3 | 2 | 4 | 4 | 9 | 4 | 2 | 7 | 0 | B | 9 | 17 | 2 |
| HE minus | 17 | 65-66 | 23r | 14 | 3 | 2 | 5 | 4 | 9 | 4 | 2 | 7 | 1 | B | 9 | 17 | 2 |
| HE minus | 17 | 65 | 19r | 17 | 3 | 3 | 0 | 5 | 9 | 4 | 2 | 6 | 0 | T | 9 | 17 | 2 |
| HE minus | 17 | 65 | 20f | 9 | 3 | 3 | 1 | 5 | 9 | 4 | 2 | 6 | 1 | T | 9 | 17 | 2 |
| HE minus | 17 | 65-66 | 21r | 1 | 3 | 3 | 2 | 5 | 9 | 4 | 2 | 6 | 2 | T | 9 | 17 | 2 |
| HE minus | 17 | 65 | 21f | 4 | 3 | 3 | 3 | 6 | 9 | 4 | 2 | 7 | 2 | T | 9 | 17 | 2 |
| HE minus | 17 | 65 | 19f | 13 | 3 | 3 | 4 | 6 | 9 | 4 | 2 | 7 | 0 | T | 9 | 17 | 2 |
| HE minus | 17 | 65-66 | 20r | 8 | 3 | 3 | 5 | 6 | 9 | 4 | 2 | 7 | 1 | T | 9 | 17 | 2 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |
| HE minus | 17 | 66 | 17 | 12 | 4 | 1 | 0 | 1 | 9 | 3 | 1 | 6 | 0 | B | 9 | 17 | 1 |
| HE minus | 17 | 66 | 16 | 7 | 4 | 1 | 1 | 1 | 9 | 3 | 1 | 6 | 1 | B | 9 | 17 | 1 |
| HE minus | 17 | 65-66 | 26f | 16 | 4 | 1 | 2 | 1 | 9 | 3 | 1 | 6 | 2 | B | 9 | 17 | 1 |
| HE minus | 17 | 65-66 | 26r | 19 | 4 | 1 | 3 | 2 | 9 | 3 | 1 | 7 | 2 | B | 9 | 17 | 1 |
| HE minus | 17 | 66 | 18f | 11 | 4 | 1 | 4 | 2 | 9 | 3 | 1 | 7 | 0 | B | 9 | 17 | 1 |
| HE minus | 17 | 66 | 18r | 3 | 4 | 1 | 5 | 2 | 9 | 3 | 1 | 7 | 1 | B | 9 | 17 | 1 |
| HE minus | 17 | 65-66 | 28f | 6 | 4 | 2 | 0 | 3 | 9 | 4 | 2 | 2 | 0 | B | 9 | 17 | 2 |
| HE minus | 17 | 65-66 | 28m | 18 | 4 | 2 | 1 | 3 | 9 | 4 | 2 | 2 | 1 | B | 9 | 17 | 2 |
| HE minus | 17 | 65-66 | 29f | 15 | 4 | 2 | 2 | 3 | 9 | 4 | 2 | 2 | 2 | B | 9 | 17 | 2 |
| HE minus | 17 | 65-66 | 28r | 14 | 4 | 2 | 3 | 4 | 9 | 4 | 2 | 3 | 2 | B | 9 | 17 | 2 |
| HE minus | 17 | 65-66 | 24r | 5 | 4 | 2 | 4 | 4 | 9 | 4 | 2 | 3 | 0 | B | 9 | 17 | 2 |
| HE minus | 17 | 65-66 | 24f | 2 | 4 | 2 | 5 | 4 | 9 | 4 | 2 | 3 | 1 | B | 9 | 17 | 2 |
| HE minus | 17 | 66 | 19r | 1 | 4 | 3 | 0 | 5 | 9 | 4 | 2 | 4 | 0 | T | 9 | 17 | 2 |
| HE minus | 17 | 66 | 20f | 9 | 4 | 3 | 1 | 5 | 9 | 4 | 2 | 4 | 1 | T | 9 | 17 | 2 |
| HE minus | 17 | 65-66 | 22r | 17 | 4 | 3 | 2 | 5 | 9 | 4 | 2 | 4 | 2 | T | 9 | 17 | 2 |
| HE minus | 17 | 66 | 19f | 4 | 4 | 3 | 3 | 6 | 9 | 4 | 2 | 5 | 2 | T | 9 | 17 | 2 |
| HE minus | 17 | 66 | 20r | 8 | 4 | 3 | 4 | 6 | 9 | 4 | 2 | 5 | 0 | T | 9 | 17 | 2 |
| HE minus | 17 | 65-66 | 22f | 13 | 4 | 3 | 5 | 6 | 9 | 4 | 2 | 5 | 1 | T | 9 | 17 | 2 |

| Det | W | Phi | tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch |
|---------|----|-----|-------|-------|----|------|-----|------|-------|-------|-----|------|-----|------|-------|-----|-----|
| HB plus | 17 | 63 | 16r | 2 | 4 | 1 | 0 | 1 | 9 | 6 | 4 | 0 | 0 | T | 9 | 17 | 4 |
| HB plus | 17 | 63 | 15r | 7 | 4 | 1 | 1 | 1 | 9 | 6 | 4 | 0 | 1 | T | 9 | 17 | 4 |
| HB plus | 17 | 63 | 14 | 6 | 4 | 1 | 2 | 1 | 9 | 6 | 4 | 0 | 2 | T | 9 | 17 | 4 |
| HB plus | 17 | 63 | 16f | 3 | 4 | 1 | 3 | 2 | 9 | 6 | 4 | 1 | 2 | T | 9 | 17 | 4 |
| HB plus | 17 | 63 | 15f | 1 | 4 | 1 | 4 | 2 | 9 | 6 | 4 | 1 | 0 | T | 9 | 17 | 4 |
| HB plus | 17 | 63 | 13 | 5 | 4 | 1 | 5 | 2 | 9 | 6 | 4 | 1 | 1 | T | 9 | 17 | 4 |
| HB plus | 17 | 63 | 10 | 11 | 4 | 2 | 0 | 3 | 9 | 5 | 3 | 1 | 0 | T | 9 | 17 | 3 |
| HB plus | 17 | 63 | 6 | 15 | 4 | 2 | 1 | 3 | 9 | 5 | 3 | 1 | 1 | T | 9 | 17 | 3 |
| HB plus | 17 | 63 | 2 | 18 | 4 | 2 | 2 | 3 | 9 | 5 | 3 | 1 | 2 | T | 9 | 17 | 3 |
| HB plus | 17 | 63 | 12 | 4 | 4 | 2 | 3 | 4 | 9 | 5 | 3 | 0 | 2 | B | 9 | 17 | 3 |
| HB plus | 17 | 63 | 8 | 8 | 4 | 2 | 4 | 4 | 9 | 5 | 3 | 0 | 0 | B | 9 | 17 | 3 |
| HB plus | 17 | 63 | 4 | 13 | 4 | 2 | 5 | 4 | 9 | 5 | 3 | 0 | 1 | B | 9 | 17 | 3 |
| HB plus | 17 | 63 | 11 | 12 | 4 | 3 | 0 | 5 | 9 | 5 | 3 | 1 | 0 | B | 9 | 17 | 3 |
| HB plus | 17 | 63 | 7 | 16 | 4 | 3 | 1 | 5 | 9 | 5 | 3 | 1 | 1 | B | 9 | 17 | 3 |
| HB plus | 17 | 63 | 3 | 19 | 4 | 3 | 2 | 5 | 9 | 5 | 3 | 1 | 2 | B | 9 | 17 | 3 |
| HB plus | 17 | 63 | 9 | 9 | 4 | 3 | 3 | 6 | 9 | 5 | 3 | 0 | 2 | T | 9 | 17 | 3 |
| HB plus | 17 | 63 | 5 | 14 | 4 | 3 | 4 | 6 | 9 | 5 | 3 | 0 | 0 | T | 9 | 17 | 3 |
| HB plus | 17 | 63 | 1 | 17 | 4 | 3 | 5 | 6 | 9 | 5 | 3 | 0 | 1 | T | 9 | 17 | 3 |
| Det | W | Phi | tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch |
| HB plus | 17 | 64 | 16r | 3 | 3 | 1 | 0 | 1 | 9 | 6 | 4 | 2 | 0 | T | 9 | 17 | 4 |
| HB plus | 17 | 64 | 15r | 17 | 3 | 1 | 1 | 1 | 9 | 6 | 4 | 2 | 1 | T | 9 | 17 | 4 |
| HB plus | 17 | 64 | 14 | 19 | 3 | 1 | 2 | 1 | 9 | 6 | 4 | 2 | 2 | T | 9 | 17 | 4 |
| HB plus | 17 | 64 | 16f | 1 | 3 | 1 | 3 | 2 | 9 | 6 | 4 | 3 | 2 | T | 9 | 17 | 4 |
| HB plus | 17 | 64 | 15f | 18 | 3 | 1 | 4 | 2 | 9 | 6 | 4 | 3 | 0 | T | 9 | 17 | 4 |
| HB plus | 17 | 64 | 13 | 13 | 3 | 1 | 5 | 2 | 9 | 6 | 4 | 3 | 1 | T | 9 | 17 | 4 |
| HB plus | 17 | 64 | 10 | 16 | 3 | 2 | 0 | 3 | 9 | 5 | 3 | 3 | 0 | T | 9 | 17 | 3 |
| HB plus | 17 | 64 | 6 | 12 | 3 | 2 | 1 | 3 | 9 | 5 | 3 | 3 | 1 | T | 9 | 17 | 3 |
| HB plus | 17 | 64 | 2 | 7 | 3 | 2 | 2 | 3 | 9 | 5 | 3 | 3 | 2 | T | 9 | 17 | 3 |
| HB plus | 17 | 64 | 12 | 14 | 3 | 2 | 3 | 4 | 9 | 5 | 3 | 2 | 2 | B | 9 | 17 | 3 |
| HB plus | 17 | 64 | 8 | 9 | 3 | 2 | 4 | 4 | 9 | 5 | 3 | 2 | 0 | B | 9 | 17 | 3 |
| HB plus | 17 | 64 | 4 | 5 | 3 | 2 | 5 | 4 | 9 | 5 | 3 | 2 | 1 | B | 9 | 17 | 3 |
| HB plus | 17 | 64 | 11 | 15 | 3 | 3 | 0 | 5 | 9 | 5 | 3 | 3 | 0 | B | 9 | 17 | 3 |
| HB plus | 17 | 64 | 7 | 11 | 3 | 3 | 1 | 5 | 9 | 5 | 3 | 3 | 1 | B | 9 | 17 | 3 |
| HB plus | 17 | 64 | 3 | 6 | 3 | 3 | 2 | 5 | 9 | 5 | 3 | 3 | 2 | B | 9 | 17 | 3 |
| HB plus | 17 | 64 | 9 | 8 | 3 | 3 | 3 | 6 | 9 | 5 | 3 | 2 | 2 | T | 9 | 17 | 3 |
| HB plus | 17 | 64 | 5 | 4 | 3 | 3 | 4 | 6 | 9 | 5 | 3 | 2 | 0 | T | 9 | 17 | 3 |
| HB plus | 17 | 64 | 1 | 2 | 3 | 3 | 5 | 6 | 9 | 5 | 3 | 2 | 1 | T | 9 | 17 | 3 |
| Det | W | Phi | tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch |
| HB plus | 17 | 65 | 16r | 17 | 2 | 1 | 0 | 1 | 9 | 6 | 4 | 0 | 0 | B | 9 | 17 | 4 |
| HB plus | 17 | 65 | 15r | 3 | 2 | 1 | 1 | 1 | 9 | 6 | 4 | 0 | 1 | B | 9 | 17 | 4 |
| HB plus | 17 | 65 | 14 | 1 | 2 | 1 | 2 | 1 | 9 | 6 | 4 | 0 | 2 | B | 9 | 17 | 4 |
| HB plus | 17 | 65 | 16f | 19 | 2 | 1 | 3 | 2 | 9 | 6 | 4 | 1 | 2 | B | 9 | 17 | 4 |
| HB plus | 17 | 65 | 15f | 2 | 2 | 1 | 4 | 2 | 9 | 6 | 4 | 1 | 0 | B | 9 | 17 | 4 |
| HB plus | 17 | 65 | 13 | 7 | 2 | 1 | 5 | 2 | 9 | 6 | 4 | 1 | 1 | B | 9 | 17 | 4 |
| HB plus | 17 | 65 | 10 | 4 | 2 | 2 | 0 | 3 | 9 | 5 | 3 | 5 | 0 | T | 9 | 17 | 3 |
| HB plus | 17 | 65 | 6 | 8 | 2 | 2 | 1 | 3 | 9 | 5 | 3 | 5 | 1 | T | 9 | 17 | 3 |
| HB plus | 17 | 65 | 2 | 13 | 2 | 2 | 2 | 3 | 9 | 5 | 3 | 5 | 2 | T | 9 | 17 | 3 |
| HB plus | 17 | 65 | 12 | 6 | 2 | 2 | 3 | 4 | 9 | 5 | 3 | 4 | 2 | B | 9 | 17 | 3 |
| HB plus | 17 | 65 | 8 | 11 | 2 | 2 | 4 | 4 | 9 | 5 | 3 | 4 | 0 | B | 9 | 17 | 3 |
| HB plus | 17 | 65 | 4 | 15 | 2 | 2 | 5 | 4 | 9 | 5 | 3 | 4 | 1 | B | 9 | 17 | 3 |

| | | | | | | | | | | | | | | | | | |
|---------|----|-----------|-------|----|------|-----|------|-------|-------|-----|------|-----|------|-------|-----|-----|---|
| HB plus | 17 | 65 | 11 | 5 | 2 | 3 | 0 | 5 | 9 | 5 | 3 | 5 | 0 | B | 9 | 17 | 3 |
| HB plus | 17 | 65 | 7 | 9 | 2 | 3 | 1 | 5 | 9 | 5 | 3 | 5 | 1 | B | 9 | 17 | 3 |
| HB plus | 17 | 65 | 3 | 14 | 2 | 3 | 2 | 5 | 9 | 5 | 3 | 5 | 2 | B | 9 | 17 | 3 |
| HB plus | 17 | 65 | 9 | 12 | 2 | 3 | 3 | 6 | 9 | 5 | 3 | 4 | 2 | T | 9 | 17 | 3 |
| HB plus | 17 | 65 | 5 | 16 | 2 | 3 | 4 | 6 | 9 | 5 | 3 | 4 | 0 | T | 9 | 17 | 3 |
| HB plus | 17 | 65 | 1 | 18 | 2 | 3 | 5 | 6 | 9 | 5 | 3 | 4 | 1 | T | 9 | 17 | 3 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |
| HB plus | 17 | 66 | 16r | 18 | 1 | 1 | 0 | 1 | 9 | 6 | 4 | 2 | 0 | B | 9 | 17 | 4 |
| HB plus | 17 | 66 | 15r | 13 | 1 | 1 | 1 | 1 | 9 | 6 | 4 | 2 | 1 | B | 9 | 17 | 4 |
| HB plus | 17 | 66 | 14 | 14 | 1 | 1 | 2 | 1 | 9 | 6 | 4 | 2 | 2 | B | 9 | 17 | 4 |
| HB plus | 17 | 66 | 16f | 17 | 1 | 1 | 3 | 2 | 9 | 6 | 4 | 3 | 2 | B | 9 | 17 | 4 |
| HB plus | 17 | 66 | 15f | 19 | 1 | 1 | 4 | 2 | 9 | 6 | 4 | 3 | 0 | B | 9 | 17 | 4 |
| HB plus | 17 | 66 | 13 | 15 | 1 | 1 | 5 | 2 | 9 | 6 | 4 | 3 | 1 | B | 9 | 17 | 4 |
| HB plus | 17 | 66 | 10 | 9 | 1 | 2 | 0 | 3 | 9 | 5 | 3 | 7 | 0 | T | 9 | 17 | 3 |
| HB plus | 17 | 66 | 6 | 5 | 1 | 2 | 1 | 3 | 9 | 5 | 3 | 7 | 1 | T | 9 | 17 | 3 |
| HB plus | 17 | 66 | 2 | 2 | 1 | 2 | 2 | 3 | 9 | 5 | 3 | 7 | 2 | T | 9 | 17 | 3 |
| HB plus | 17 | 66 | 12 | 16 | 1 | 2 | 3 | 4 | 9 | 5 | 3 | 6 | 2 | B | 9 | 17 | 3 |
| HB plus | 17 | 66 | 8 | 12 | 1 | 2 | 4 | 4 | 9 | 5 | 3 | 6 | 0 | B | 9 | 17 | 3 |
| HB plus | 17 | 66 | 4 | 7 | 1 | 2 | 5 | 4 | 9 | 5 | 3 | 6 | 1 | B | 9 | 17 | 3 |
| HB plus | 17 | 66 | 11 | 8 | 1 | 3 | 0 | 5 | 9 | 5 | 3 | 7 | 0 | B | 9 | 17 | 3 |
| HB plus | 17 | 66 | 7 | 4 | 1 | 3 | 1 | 5 | 9 | 5 | 3 | 7 | 1 | B | 9 | 17 | 3 |
| HB plus | 17 | 66 | 3 | 1 | 1 | 3 | 2 | 5 | 9 | 5 | 3 | 7 | 2 | B | 9 | 17 | 3 |
| HB plus | 17 | 66 | 9 | 11 | 1 | 3 | 3 | 6 | 9 | 5 | 3 | 6 | 2 | T | 9 | 17 | 3 |
| HB plus | 17 | 66 | 5 | 6 | 1 | 3 | 4 | 6 | 9 | 5 | 3 | 6 | 0 | T | 9 | 17 | 3 |
| HB plus | 17 | 66 | 1 | 3 | 1 | 3 | 5 | 6 | 9 | 5 | 3 | 6 | 1 | T | 9 | 17 | 3 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |
| HE plus | 17 | 63 | 17 | 12 | 4 | 1 | 0 | 1 | 9 | 6 | 4 | 4 | 0 | T | 9 | 17 | 4 |
| HE plus | 17 | 63 | 16 | 7 | 4 | 1 | 1 | 1 | 9 | 6 | 4 | 4 | 1 | T | 9 | 17 | 4 |
| HE plus | 17 | 63-64 | 26f | 16 | 4 | 1 | 2 | 1 | 9 | 6 | 4 | 4 | 2 | T | 9 | 17 | 4 |
| HE plus | 17 | 63-64 | 26r | 19 | 4 | 1 | 3 | 2 | 9 | 6 | 4 | 5 | 2 | T | 9 | 17 | 4 |
| HE plus | 17 | 63 | 18f | 11 | 4 | 1 | 4 | 2 | 9 | 6 | 4 | 5 | 0 | T | 9 | 17 | 4 |
| HE plus | 17 | 63 | 18r | 3 | 4 | 1 | 5 | 2 | 9 | 6 | 4 | 5 | 1 | T | 9 | 17 | 4 |
| HE plus | 17 | 63-64 | 28f | 6 | 4 | 2 | 0 | 3 | 9 | 7 | 5 | 0 | 0 | B | 9 | 17 | 5 |
| HE plus | 17 | 63-64 | 28m | 18 | 4 | 2 | 1 | 3 | 9 | 7 | 5 | 0 | 1 | B | 9 | 17 | 5 |
| HE plus | 17 | 63-64 | 29f | 15 | 4 | 2 | 2 | 3 | 9 | 7 | 5 | 0 | 2 | B | 9 | 17 | 5 |
| HE plus | 17 | 63-64 | 28r | 14 | 4 | 2 | 3 | 4 | 9 | 7 | 5 | 1 | 2 | B | 9 | 17 | 5 |
| HE plus | 17 | 63-64 | 24r | 5 | 4 | 2 | 4 | 4 | 9 | 7 | 5 | 1 | 0 | B | 9 | 17 | 5 |
| HE plus | 17 | 63-64 | 24f | 2 | 4 | 2 | 5 | 4 | 9 | 7 | 5 | 1 | 1 | B | 9 | 17 | 5 |
| HE plus | 17 | 63 | 19r | 1 | 4 | 3 | 0 | 5 | 9 | 7 | 5 | 0 | 0 | T | 9 | 17 | 5 |
| HE plus | 17 | 63 | 20f | 9 | 4 | 3 | 1 | 5 | 9 | 7 | 5 | 0 | 1 | T | 9 | 17 | 5 |
| HE plus | 17 | 63-64 | 22r | 17 | 4 | 3 | 2 | 5 | 9 | 7 | 5 | 0 | 2 | T | 9 | 17 | 5 |
| HE plus | 17 | 63 | 19f | 4 | 4 | 3 | 3 | 6 | 9 | 7 | 5 | 1 | 2 | T | 9 | 17 | 5 |
| HE plus | 17 | 63 | 20r | 8 | 4 | 3 | 4 | 6 | 9 | 7 | 5 | 1 | 0 | T | 9 | 17 | 5 |
| HE plus | 17 | 63-64 | 22f | 13 | 4 | 3 | 5 | 6 | 9 | 7 | 5 | 1 | 1 | T | 9 | 17 | 5 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |
| HE plus | 17 | 64 | 17 | 12 | 3 | 1 | 0 | 1 | 9 | 6 | 4 | 6 | 0 | T | 9 | 17 | 4 |
| HE plus | 17 | 64 | 16 | 16 | 3 | 1 | 1 | 1 | 9 | 6 | 4 | 6 | 1 | T | 9 | 17 | 4 |
| HE plus | 17 | 63-64 | 25f | 7 | 3 | 1 | 2 | 1 | 9 | 6 | 4 | 6 | 2 | T | 9 | 17 | 4 |
| HE plus | 17 | 63-64 | 25r | 3 | 3 | 1 | 3 | 2 | 9 | 6 | 4 | 7 | 2 | T | 9 | 17 | 4 |
| HE plus | 17 | 64 | 18f | 11 | 3 | 1 | 4 | 2 | 9 | 6 | 4 | 7 | 0 | T | 9 | 17 | 4 |
| HE plus | 17 | 64 | 18r | 19 | 3 | 1 | 5 | 2 | 9 | 6 | 4 | 7 | 1 | T | 9 | 17 | 4 |

| | | | | | | | | | | | | | | | | | |
|---------|----|-----------|-------|----|------|-----|------|-------|-------|-----|------|-----|------|-------|-----|-----|---|
| HE plus | 17 | 63-64 | 27f | 6 | 3 | 2 | 0 | 3 | 9 | 7 | 5 | 4 | 0 | B | 9 | 17 | 5 |
| HE plus | 17 | 63-64 | 27m | 2 | 3 | 2 | 1 | 3 | 9 | 7 | 5 | 4 | 1 | B | 9 | 17 | 5 |
| HE plus | 17 | 63-64 | 29m | 15 | 3 | 2 | 2 | 3 | 9 | 7 | 5 | 4 | 2 | B | 9 | 17 | 5 |
| HE plus | 17 | 63-64 | 23f | 18 | 3 | 2 | 3 | 4 | 9 | 7 | 5 | 5 | 2 | B | 9 | 17 | 5 |
| HE plus | 17 | 63-64 | 27r | 5 | 3 | 2 | 4 | 4 | 9 | 7 | 5 | 5 | 0 | B | 9 | 17 | 5 |
| HE plus | 17 | 63-64 | 23r | 14 | 3 | 2 | 5 | 4 | 9 | 7 | 5 | 5 | 1 | B | 9 | 17 | 5 |
| HE plus | 17 | 64 | 19r | 17 | 3 | 3 | 0 | 5 | 9 | 7 | 5 | 2 | 0 | T | 9 | 17 | 5 |
| HE plus | 17 | 64 | 20f | 9 | 3 | 3 | 1 | 5 | 9 | 7 | 5 | 2 | 1 | T | 9 | 17 | 5 |
| HE plus | 17 | 63-64 | 21r | 1 | 3 | 3 | 2 | 5 | 9 | 7 | 5 | 2 | 2 | T | 9 | 17 | 5 |
| HE plus | 17 | 64 | 21f | 4 | 3 | 3 | 3 | 6 | 9 | 7 | 5 | 3 | 2 | T | 9 | 17 | 5 |
| HE plus | 17 | 64 | 19f | 13 | 3 | 3 | 4 | 6 | 9 | 7 | 5 | 3 | 0 | T | 9 | 17 | 5 |
| HE plus | 17 | 63-64 | 20r | 8 | 3 | 3 | 5 | 6 | 9 | 7 | 5 | 3 | 1 | T | 9 | 17 | 5 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |
| HE plus | 17 | 65 | 17 | 12 | 2 | 1 | 0 | 1 | 9 | 6 | 4 | 4 | 0 | B | 9 | 17 | 4 |
| HE plus | 17 | 65 | 16 | 7 | 2 | 1 | 1 | 1 | 9 | 6 | 4 | 4 | 1 | B | 9 | 17 | 4 |
| HE plus | 17 | 65-66 | 25f | 16 | 2 | 1 | 2 | 1 | 9 | 6 | 4 | 4 | 2 | B | 9 | 17 | 4 |
| HE plus | 17 | 65-66 | 25r | 19 | 2 | 1 | 3 | 2 | 9 | 6 | 4 | 5 | 2 | B | 9 | 17 | 4 |
| HE plus | 17 | 65 | 18f | 11 | 2 | 1 | 4 | 2 | 9 | 6 | 4 | 5 | 0 | B | 9 | 17 | 4 |
| HE plus | 17 | 65 | 18r | 3 | 2 | 1 | 5 | 2 | 9 | 6 | 4 | 5 | 1 | B | 9 | 17 | 4 |
| HE plus | 17 | 65-66 | 27f | 15 | 2 | 2 | 0 | 3 | 9 | 7 | 5 | 6 | 0 | B | 9 | 17 | 5 |
| HE plus | 17 | 65-66 | 27m | 18 | 2 | 2 | 1 | 3 | 9 | 7 | 5 | 6 | 1 | B | 9 | 17 | 5 |
| HE plus | 17 | 65-66 | 29m | 6 | 2 | 2 | 2 | 3 | 9 | 7 | 5 | 6 | 2 | B | 9 | 17 | 5 |
| HE plus | 17 | 65-66 | 23f | 2 | 2 | 2 | 3 | 4 | 9 | 7 | 5 | 7 | 2 | B | 9 | 17 | 5 |
| HE plus | 17 | 65-66 | 27r | 14 | 2 | 2 | 4 | 4 | 9 | 7 | 5 | 7 | 0 | B | 9 | 17 | 5 |
| HE plus | 17 | 65-66 | 23r | 5 | 2 | 2 | 5 | 4 | 9 | 7 | 5 | 7 | 1 | B | 9 | 17 | 5 |
| HE plus | 17 | 65 | 19r | 1 | 2 | 3 | 0 | 5 | 9 | 7 | 5 | 6 | 0 | T | 9 | 17 | 5 |
| HE plus | 17 | 65 | 20f | 9 | 2 | 3 | 1 | 5 | 9 | 7 | 5 | 6 | 1 | T | 9 | 17 | 5 |
| HE plus | 17 | 65-66 | 21r | 17 | 2 | 3 | 2 | 5 | 9 | 7 | 5 | 6 | 2 | T | 9 | 17 | 5 |
| HE plus | 17 | 65 | 21f | 13 | 2 | 3 | 3 | 6 | 9 | 7 | 5 | 7 | 2 | T | 9 | 17 | 5 |
| HE plus | 17 | 65 | 19f | 4 | 2 | 3 | 4 | 6 | 9 | 7 | 5 | 7 | 0 | T | 9 | 17 | 5 |
| HE plus | 17 | 65-66 | 20r | 8 | 2 | 3 | 5 | 6 | 9 | 7 | 5 | 7 | 1 | T | 9 | 17 | 5 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |
| HE plus | 17 | 66 | 17 | 12 | 1 | 1 | 0 | 1 | 9 | 6 | 4 | 6 | 0 | B | 9 | 17 | 4 |
| HE plus | 17 | 66 | 16 | 16 | 1 | 1 | 1 | 1 | 9 | 6 | 4 | 6 | 1 | B | 9 | 17 | 4 |
| HE plus | 17 | 65-66 | 26f | 7 | 1 | 1 | 2 | 1 | 9 | 6 | 4 | 6 | 2 | B | 9 | 17 | 4 |
| HE plus | 17 | 65-66 | 26r | 3 | 1 | 1 | 3 | 2 | 9 | 6 | 4 | 7 | 2 | B | 9 | 17 | 4 |
| HE plus | 17 | 66 | 18f | 11 | 1 | 1 | 4 | 2 | 9 | 6 | 4 | 7 | 0 | B | 9 | 17 | 4 |
| HE plus | 17 | 66 | 18r | 19 | 1 | 1 | 5 | 2 | 9 | 6 | 4 | 7 | 1 | B | 9 | 17 | 4 |
| HE plus | 17 | 65-66 | 28f | 15 | 1 | 2 | 0 | 3 | 9 | 7 | 5 | 2 | 0 | B | 9 | 17 | 5 |
| HE plus | 17 | 65-66 | 28m | 2 | 1 | 2 | 1 | 3 | 9 | 7 | 5 | 2 | 1 | B | 9 | 17 | 5 |
| HE plus | 17 | 65-66 | 29f | 6 | 1 | 2 | 2 | 3 | 9 | 7 | 5 | 2 | 2 | B | 9 | 17 | 5 |
| HE plus | 17 | 65-66 | 28r | 5 | 1 | 2 | 3 | 4 | 9 | 7 | 5 | 3 | 2 | B | 9 | 17 | 5 |
| HE plus | 17 | 65-66 | 24r | 14 | 1 | 2 | 4 | 4 | 9 | 7 | 5 | 3 | 0 | B | 9 | 17 | 5 |
| HE plus | 17 | 65-66 | 24f | 18 | 1 | 2 | 5 | 4 | 9 | 7 | 5 | 3 | 1 | B | 9 | 17 | 5 |
| HE plus | 17 | 66 | 19r | 17 | 1 | 3 | 0 | 5 | 9 | 7 | 5 | 4 | 0 | T | 9 | 17 | 5 |
| HE plus | 17 | 66 | 20f | 9 | 1 | 3 | 1 | 5 | 9 | 7 | 5 | 4 | 1 | T | 9 | 17 | 5 |
| HE plus | 17 | 65-66 | 22r | 4 | 1 | 3 | 2 | 5 | 9 | 7 | 5 | 4 | 2 | T | 9 | 17 | 5 |
| HE plus | 17 | 66 | 19f | 13 | 1 | 3 | 3 | 6 | 9 | 7 | 5 | 5 | 2 | T | 9 | 17 | 5 |
| HE plus | 17 | 66 | 20r | 8 | 1 | 3 | 4 | 6 | 9 | 7 | 5 | 5 | 0 | T | 9 | 17 | 5 |
| HE plus | 17 | 65-66 | 22f | 1 | 1 | 3 | 5 | 6 | 9 | 7 | 5 | 5 | 1 | T | 9 | 17 | 5 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |

| | | | | | | | | | | | | | | | | | |
|----------|----|-----------|-------|----|------|-----|------|-------|-------|-----|------|-----|------|-------|-----|-----|---|
| HB minus | 18 | 67 | 16r | 18 | 1 | 1 | 0 | 1 | 9 | 14 | 7 | 0 | 0 | T | 19 | 18 | 7 |
| HB minus | 18 | 67 | 15r | 13 | 1 | 1 | 1 | 1 | 9 | 14 | 7 | 0 | 1 | T | 19 | 18 | 7 |
| HB minus | 18 | 67 | 14 | 14 | 1 | 1 | 2 | 1 | 9 | 14 | 7 | 0 | 2 | T | 19 | 18 | 7 |
| HB minus | 18 | 67 | 16f | 17 | 1 | 1 | 3 | 2 | 9 | 14 | 7 | 1 | 2 | T | 19 | 18 | 7 |
| HB minus | 18 | 67 | 15f | 19 | 1 | 1 | 4 | 2 | 9 | 14 | 7 | 1 | 0 | T | 19 | 18 | 7 |
| HB minus | 18 | 67 | 13 | 15 | 1 | 1 | 5 | 2 | 9 | 14 | 7 | 1 | 1 | T | 19 | 18 | 7 |
| HB minus | 18 | 67 | 10 | 9 | 1 | 2 | 0 | 3 | 9 | 13 | 6 | 1 | 0 | T | 19 | 18 | 6 |
| HB minus | 18 | 67 | 6 | 5 | 1 | 2 | 1 | 3 | 9 | 13 | 6 | 1 | 1 | T | 19 | 18 | 6 |
| HB minus | 18 | 67 | 2 | 2 | 1 | 2 | 2 | 3 | 9 | 13 | 6 | 1 | 2 | T | 19 | 18 | 6 |
| HB minus | 18 | 67 | 12 | 16 | 1 | 2 | 3 | 4 | 9 | 13 | 6 | 0 | 2 | B | 19 | 18 | 6 |
| HB minus | 18 | 67 | 8 | 12 | 1 | 2 | 4 | 4 | 9 | 13 | 6 | 0 | 0 | B | 19 | 18 | 6 |
| HB minus | 18 | 67 | 4 | 7 | 1 | 2 | 5 | 4 | 9 | 13 | 6 | 0 | 1 | B | 19 | 18 | 6 |
| HB minus | 18 | 67 | 11 | 8 | 1 | 3 | 0 | 5 | 9 | 13 | 6 | 1 | 0 | B | 19 | 18 | 6 |
| HB minus | 18 | 67 | 7 | 4 | 1 | 3 | 1 | 5 | 9 | 13 | 6 | 1 | 1 | B | 19 | 18 | 6 |
| HB minus | 18 | 67 | 3 | 1 | 1 | 3 | 2 | 5 | 9 | 13 | 6 | 1 | 2 | B | 19 | 18 | 6 |
| HB minus | 18 | 67 | 9 | 11 | 1 | 3 | 3 | 6 | 9 | 13 | 6 | 0 | 2 | T | 19 | 18 | 6 |
| HB minus | 18 | 67 | 5 | 6 | 1 | 3 | 4 | 6 | 9 | 13 | 6 | 0 | 0 | T | 19 | 18 | 6 |
| HB minus | 18 | 67 | 1 | 3 | 1 | 3 | 5 | 6 | 9 | 13 | 6 | 0 | 1 | T | 19 | 18 | 6 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |
| HB minus | 18 | 68 | 16r | 17 | 2 | 1 | 0 | 1 | 9 | 14 | 7 | 2 | 0 | T | 19 | 18 | 7 |
| HB minus | 18 | 68 | 15r | 3 | 2 | 1 | 1 | 1 | 9 | 14 | 7 | 2 | 1 | T | 19 | 18 | 7 |
| HB minus | 18 | 68 | 14 | 1 | 2 | 1 | 2 | 1 | 9 | 14 | 7 | 2 | 2 | T | 19 | 18 | 7 |
| HB minus | 18 | 68 | 16f | 19 | 2 | 1 | 3 | 2 | 9 | 14 | 7 | 3 | 2 | T | 19 | 18 | 7 |
| HB minus | 18 | 68 | 15f | 2 | 2 | 1 | 4 | 2 | 9 | 14 | 7 | 3 | 0 | T | 19 | 18 | 7 |
| HB minus | 18 | 68 | 13 | 7 | 2 | 1 | 5 | 2 | 9 | 14 | 7 | 3 | 1 | T | 19 | 18 | 7 |
| HB minus | 18 | 68 | 10 | 4 | 2 | 2 | 0 | 3 | 9 | 13 | 6 | 3 | 0 | T | 19 | 18 | 6 |
| HB minus | 18 | 68 | 6 | 8 | 2 | 2 | 1 | 3 | 9 | 13 | 6 | 3 | 1 | T | 19 | 18 | 6 |
| HB minus | 18 | 68 | 2 | 13 | 2 | 2 | 2 | 3 | 9 | 13 | 6 | 3 | 2 | T | 19 | 18 | 6 |
| HB minus | 18 | 68 | 12 | 6 | 2 | 2 | 3 | 4 | 9 | 13 | 6 | 2 | 2 | B | 19 | 18 | 6 |
| HB minus | 18 | 68 | 8 | 11 | 2 | 2 | 4 | 4 | 9 | 13 | 6 | 2 | 0 | B | 19 | 18 | 6 |
| HB minus | 18 | 68 | 4 | 15 | 2 | 2 | 5 | 4 | 9 | 13 | 6 | 2 | 1 | B | 19 | 18 | 6 |
| HB minus | 18 | 68 | 11 | 5 | 2 | 3 | 0 | 5 | 9 | 13 | 6 | 3 | 0 | B | 19 | 18 | 6 |
| HB minus | 18 | 68 | 7 | 9 | 2 | 3 | 1 | 5 | 9 | 13 | 6 | 3 | 1 | B | 19 | 18 | 6 |
| HB minus | 18 | 68 | 3 | 14 | 2 | 3 | 2 | 5 | 9 | 13 | 6 | 3 | 2 | B | 19 | 18 | 6 |
| HB minus | 18 | 68 | 9 | 12 | 2 | 3 | 3 | 6 | 9 | 13 | 6 | 2 | 2 | T | 19 | 18 | 6 |
| HB minus | 18 | 68 | 5 | 16 | 2 | 3 | 4 | 6 | 9 | 13 | 6 | 2 | 0 | T | 19 | 18 | 6 |
| HB minus | 18 | 68 | 1 | 18 | 2 | 3 | 5 | 6 | 9 | 13 | 6 | 2 | 1 | T | 19 | 18 | 6 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |
| HB minus | 18 | 69 | 16r | 3 | 3 | 1 | 0 | 1 | 9 | 14 | 7 | 0 | 0 | B | 19 | 18 | 7 |
| HB minus | 18 | 69 | 15r | 17 | 3 | 1 | 1 | 1 | 9 | 14 | 7 | 0 | 1 | B | 19 | 18 | 7 |
| HB minus | 18 | 69 | 14 | 19 | 3 | 1 | 2 | 1 | 9 | 14 | 7 | 0 | 2 | B | 19 | 18 | 7 |
| HB minus | 18 | 69 | 16f | 1 | 3 | 1 | 3 | 2 | 9 | 14 | 7 | 1 | 2 | B | 19 | 18 | 7 |
| HB minus | 18 | 69 | 15f | 18 | 3 | 1 | 4 | 2 | 9 | 14 | 7 | 1 | 0 | B | 19 | 18 | 7 |
| HB minus | 18 | 69 | 13 | 13 | 3 | 1 | 5 | 2 | 9 | 14 | 7 | 1 | 1 | B | 19 | 18 | 7 |
| HB minus | 18 | 69 | 10 | 16 | 3 | 2 | 0 | 3 | 9 | 13 | 6 | 5 | 0 | T | 19 | 18 | 6 |
| HB minus | 18 | 69 | 6 | 12 | 3 | 2 | 1 | 3 | 9 | 13 | 6 | 5 | 1 | T | 19 | 18 | 6 |
| HB minus | 18 | 69 | 2 | 7 | 3 | 2 | 2 | 3 | 9 | 13 | 6 | 5 | 2 | T | 19 | 18 | 6 |
| HB minus | 18 | 69 | 12 | 14 | 3 | 2 | 3 | 4 | 9 | 13 | 6 | 4 | 2 | B | 19 | 18 | 6 |
| HB minus | 18 | 69 | 8 | 9 | 3 | 2 | 4 | 4 | 9 | 13 | 6 | 4 | 0 | B | 19 | 18 | 6 |
| HB minus | 18 | 69 | 4 | 5 | 3 | 2 | 5 | 4 | 9 | 13 | 6 | 4 | 1 | B | 19 | 18 | 6 |
| HB minus | 18 | 69 | 11 | 15 | 3 | 3 | 0 | 5 | 9 | 13 | 6 | 5 | 0 | B | 19 | 18 | 6 |

| | | | | | | | | | | | | | | | | | |
|----------|----|-----------|-------|----|------|-----|------|-------|-------|-----|------|-----|------|-------|-----|-----|---|
| HB minus | 18 | 69 | 7 | 11 | 3 | 3 | 1 | 5 | 9 | 13 | 6 | 5 | 1 | B | 19 | 18 | 6 |
| HB minus | 18 | 69 | 3 | 6 | 3 | 3 | 2 | 5 | 9 | 13 | 6 | 5 | 2 | B | 19 | 18 | 6 |
| HB minus | 18 | 69 | 9 | 8 | 3 | 3 | 3 | 6 | 9 | 13 | 6 | 4 | 2 | T | 19 | 18 | 6 |
| HB minus | 18 | 69 | 5 | 4 | 3 | 3 | 4 | 6 | 9 | 13 | 6 | 4 | 0 | T | 19 | 18 | 6 |
| HB minus | 18 | 69 | 1 | 2 | 3 | 3 | 5 | 6 | 9 | 13 | 6 | 4 | 1 | T | 19 | 18 | 6 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |
| HB minus | 18 | 70 | 16r | 2 | 4 | 1 | 0 | 1 | 9 | 14 | 7 | 2 | 0 | B | 19 | 18 | 7 |
| HB minus | 18 | 70 | 15r | 7 | 4 | 1 | 1 | 1 | 9 | 14 | 7 | 2 | 1 | B | 19 | 18 | 7 |
| HB minus | 18 | 70 | 14 | 6 | 4 | 1 | 2 | 1 | 9 | 14 | 7 | 2 | 2 | B | 19 | 18 | 7 |
| HB minus | 18 | 70 | 16f | 3 | 4 | 1 | 3 | 2 | 9 | 14 | 7 | 3 | 2 | B | 19 | 18 | 7 |
| HB minus | 18 | 70 | 15f | 1 | 4 | 1 | 4 | 2 | 9 | 14 | 7 | 3 | 0 | B | 19 | 18 | 7 |
| HB minus | 18 | 70 | 13 | 5 | 4 | 1 | 5 | 2 | 9 | 14 | 7 | 3 | 1 | B | 19 | 18 | 7 |
| HB minus | 18 | 70 | 10 | 11 | 4 | 2 | 0 | 3 | 9 | 13 | 6 | 7 | 0 | T | 19 | 18 | 6 |
| HB minus | 18 | 70 | 6 | 15 | 4 | 2 | 1 | 3 | 9 | 13 | 6 | 7 | 1 | T | 19 | 18 | 6 |
| HB minus | 18 | 70 | 2 | 18 | 4 | 2 | 2 | 3 | 9 | 13 | 6 | 7 | 2 | T | 19 | 18 | 6 |
| HB minus | 18 | 70 | 12 | 4 | 4 | 2 | 3 | 4 | 9 | 13 | 6 | 6 | 2 | B | 19 | 18 | 6 |
| HB minus | 18 | 70 | 8 | 8 | 4 | 2 | 4 | 4 | 9 | 13 | 6 | 6 | 0 | B | 19 | 18 | 6 |
| HB minus | 18 | 70 | 4 | 13 | 4 | 2 | 5 | 4 | 9 | 13 | 6 | 6 | 1 | B | 19 | 18 | 6 |
| HB minus | 18 | 70 | 11 | 12 | 4 | 3 | 0 | 5 | 9 | 13 | 6 | 7 | 0 | B | 19 | 18 | 6 |
| HB minus | 18 | 70 | 7 | 16 | 4 | 3 | 1 | 5 | 9 | 13 | 6 | 7 | 1 | B | 19 | 18 | 6 |
| HB minus | 18 | 70 | 3 | 19 | 4 | 3 | 2 | 5 | 9 | 13 | 6 | 7 | 2 | B | 19 | 18 | 6 |
| HB minus | 18 | 70 | 9 | 9 | 4 | 3 | 3 | 6 | 9 | 13 | 6 | 6 | 2 | T | 19 | 18 | 6 |
| HB minus | 18 | 70 | 5 | 14 | 4 | 3 | 4 | 6 | 9 | 13 | 6 | 6 | 0 | T | 19 | 18 | 6 |
| HB minus | 18 | 70 | 1 | 17 | 4 | 3 | 5 | 6 | 9 | 13 | 6 | 6 | 1 | T | 19 | 18 | 6 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |
| HE minus | 18 | 67 | 17 | 12 | 1 | 1 | 0 | 1 | 9 | 14 | 7 | 4 | 0 | T | 19 | 18 | 7 |
| HE minus | 18 | 67 | 16 | 16 | 1 | 1 | 1 | 1 | 9 | 14 | 7 | 4 | 1 | T | 19 | 18 | 7 |
| HE minus | 18 | 67-68 | 26f | 7 | 1 | 1 | 2 | 1 | 9 | 14 | 7 | 4 | 2 | T | 19 | 18 | 7 |
| HE minus | 18 | 67-68 | 26r | 3 | 1 | 1 | 3 | 2 | 9 | 14 | 7 | 5 | 2 | T | 19 | 18 | 7 |
| HE minus | 18 | 67 | 18f | 11 | 1 | 1 | 4 | 2 | 9 | 14 | 7 | 5 | 0 | T | 19 | 18 | 7 |
| HE minus | 18 | 67 | 18r | 19 | 1 | 1 | 5 | 2 | 9 | 14 | 7 | 5 | 1 | T | 19 | 18 | 7 |
| HE minus | 18 | 67-68 | 28f | 15 | 1 | 2 | 0 | 3 | 9 | 15 | 8 | 0 | 0 | B | 19 | 18 | 8 |
| HE minus | 18 | 67-68 | 28m | 2 | 1 | 2 | 1 | 3 | 9 | 15 | 8 | 0 | 1 | B | 19 | 18 | 8 |
| HE minus | 18 | 67-68 | 29f | 6 | 1 | 2 | 2 | 3 | 9 | 15 | 8 | 0 | 2 | B | 19 | 18 | 8 |
| HE minus | 18 | 67-68 | 28r | 5 | 1 | 2 | 3 | 4 | 9 | 15 | 8 | 1 | 2 | B | 19 | 18 | 8 |
| HE minus | 18 | 67-68 | 24r | 14 | 1 | 2 | 4 | 4 | 9 | 15 | 8 | 1 | 0 | B | 19 | 18 | 8 |
| HE minus | 18 | 67-68 | 24f | 18 | 1 | 2 | 5 | 4 | 9 | 15 | 8 | 1 | 1 | B | 19 | 18 | 8 |
| HE minus | 18 | 67 | 19r | 17 | 1 | 3 | 0 | 5 | 9 | 15 | 8 | 0 | 0 | T | 19 | 18 | 8 |
| HE minus | 18 | 67 | 20f | 9 | 1 | 3 | 1 | 5 | 9 | 15 | 8 | 0 | 1 | T | 19 | 18 | 8 |
| HE minus | 18 | 67-68 | 22r | 4 | 1 | 3 | 2 | 5 | 9 | 15 | 8 | 0 | 2 | T | 19 | 18 | 8 |
| HE minus | 18 | 67-68 | 19f | 13 | 1 | 3 | 3 | 6 | 9 | 15 | 8 | 1 | 2 | T | 19 | 18 | 8 |
| HE minus | 18 | 67 | 20r | 8 | 1 | 3 | 4 | 6 | 9 | 15 | 8 | 1 | 0 | T | 19 | 18 | 8 |
| HE minus | 18 | 67-68 | 22f | 1 | 1 | 3 | 5 | 6 | 9 | 15 | 8 | 1 | 1 | T | 19 | 18 | 8 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |
| HE minus | 18 | 68 | 17 | 12 | 2 | 1 | 0 | 1 | 9 | 14 | 7 | 6 | 0 | T | 19 | 18 | 7 |
| HE minus | 18 | 68 | 16 | 7 | 2 | 1 | 1 | 1 | 9 | 14 | 7 | 6 | 1 | T | 19 | 18 | 7 |
| HE minus | 18 | 67-68 | 25f | 16 | 2 | 1 | 2 | 1 | 9 | 14 | 7 | 6 | 2 | T | 19 | 18 | 7 |
| HE minus | 18 | 67-68 | 25r | 19 | 2 | 1 | 3 | 2 | 9 | 14 | 7 | 7 | 2 | T | 19 | 18 | 7 |
| HE minus | 18 | 68 | 18f | 11 | 2 | 1 | 4 | 2 | 9 | 14 | 7 | 7 | 0 | T | 19 | 18 | 7 |
| HE minus | 18 | 68 | 18r | 3 | 2 | 1 | 5 | 2 | 9 | 14 | 7 | 7 | 1 | T | 19 | 18 | 7 |
| HE minus | 18 | 67-68 | 27f | 15 | 2 | 2 | 0 | 3 | 9 | 15 | 8 | 4 | 0 | B | 19 | 18 | 8 |

| | | | | | | | | | | | | | | | | | |
|----------|----|-----------|-------|----|------|-----|------|-------|-------|-----|------|-----|------|-------|-----|-----|----|
| HE minus | 18 | 67-68 | 27m | 18 | 2 | 2 | 1 | 3 | 9 | 15 | 8 | 4 | 1 | B | 19 | 18 | 8 |
| HE minus | 18 | 67-68 | 29m | 6 | 2 | 2 | 2 | 3 | 9 | 15 | 8 | 4 | 2 | B | 19 | 18 | 8 |
| HE minus | 18 | 67-68 | 23f | 2 | 2 | 2 | 3 | 4 | 9 | 15 | 8 | 5 | 2 | B | 19 | 18 | 8 |
| HE minus | 18 | 67-68 | 27r | 14 | 2 | 2 | 4 | 4 | 9 | 15 | 8 | 5 | 0 | B | 19 | 18 | 8 |
| HE minus | 18 | 67-68 | 23r | 5 | 2 | 2 | 5 | 4 | 9 | 15 | 8 | 5 | 1 | B | 19 | 18 | 8 |
| HE minus | 18 | 68 | 19r | 1 | 2 | 3 | 0 | 5 | 9 | 15 | 8 | 2 | 0 | T | 19 | 18 | 8 |
| HE minus | 18 | 68 | 20f | 9 | 2 | 3 | 1 | 5 | 9 | 15 | 8 | 2 | 1 | T | 19 | 18 | 8 |
| HE minus | 18 | 67-68 | 21r | 17 | 2 | 3 | 2 | 5 | 9 | 15 | 8 | 2 | 2 | T | 19 | 18 | 8 |
| HE minus | 18 | 67-68 | 21f | 13 | 2 | 3 | 3 | 6 | 9 | 15 | 8 | 3 | 2 | T | 19 | 18 | 8 |
| HE minus | 18 | 68 | 19f | 4 | 2 | 3 | 4 | 6 | 9 | 15 | 8 | 3 | 0 | T | 19 | 18 | 8 |
| HE minus | 18 | 67-68 | 20r | 8 | 2 | 3 | 5 | 6 | 9 | 15 | 8 | 3 | 1 | T | 19 | 18 | 8 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |
| HE minus | 18 | 69 | 17 | 12 | 3 | 1 | 0 | 1 | 9 | 14 | 7 | 4 | 0 | B | 19 | 18 | 7 |
| HE minus | 18 | 69 | 16 | 16 | 3 | 1 | 1 | 1 | 9 | 14 | 7 | 4 | 1 | B | 19 | 18 | 7 |
| HE minus | 18 | 69-70 | 25f | 7 | 3 | 1 | 2 | 1 | 9 | 14 | 7 | 4 | 2 | B | 19 | 18 | 7 |
| HE minus | 18 | 69-70 | 25r | 3 | 3 | 1 | 3 | 2 | 9 | 14 | 7 | 5 | 2 | B | 19 | 18 | 7 |
| HE minus | 18 | 69 | 18f | 11 | 3 | 1 | 4 | 2 | 9 | 14 | 7 | 5 | 0 | B | 19 | 18 | 7 |
| HE minus | 18 | 69 | 18r | 19 | 3 | 1 | 5 | 2 | 9 | 14 | 7 | 5 | 1 | B | 19 | 18 | 7 |
| HE minus | 18 | 69-70 | 27f | 6 | 3 | 2 | 0 | 3 | 9 | 15 | 8 | 6 | 0 | B | 19 | 18 | 8 |
| HE minus | 18 | 69-70 | 27m | 2 | 3 | 2 | 1 | 3 | 9 | 15 | 8 | 6 | 1 | B | 19 | 18 | 8 |
| HE minus | 18 | 69-70 | 29m | 15 | 3 | 2 | 2 | 3 | 9 | 15 | 8 | 6 | 2 | B | 19 | 18 | 8 |
| HE minus | 18 | 69-70 | 23f | 18 | 3 | 2 | 3 | 4 | 9 | 15 | 8 | 7 | 2 | B | 19 | 18 | 8 |
| HE minus | 18 | 69-70 | 27r | 5 | 3 | 2 | 4 | 4 | 9 | 15 | 8 | 7 | 0 | B | 19 | 18 | 8 |
| HE minus | 18 | 69-70 | 23r | 14 | 3 | 2 | 5 | 4 | 9 | 15 | 8 | 7 | 1 | B | 19 | 18 | 8 |
| HE minus | 18 | 69 | 19r | 17 | 3 | 3 | 0 | 5 | 9 | 15 | 8 | 6 | 0 | T | 19 | 18 | 8 |
| HE minus | 18 | 69 | 20f | 9 | 3 | 3 | 1 | 5 | 9 | 15 | 8 | 6 | 1 | T | 19 | 18 | 8 |
| HE minus | 18 | 69-70 | 21r | 1 | 3 | 3 | 2 | 5 | 9 | 15 | 8 | 6 | 2 | T | 19 | 18 | 8 |
| HE minus | 18 | 69-70 | 21f | 4 | 3 | 3 | 3 | 6 | 9 | 15 | 8 | 7 | 2 | T | 19 | 18 | 8 |
| HE minus | 18 | 69 | 19f | 13 | 3 | 3 | 4 | 6 | 9 | 15 | 8 | 7 | 0 | T | 19 | 18 | 8 |
| HE minus | 18 | 69-70 | 20r | 8 | 3 | 3 | 5 | 6 | 9 | 15 | 8 | 7 | 1 | T | 19 | 18 | 8 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |
| HE minus | 18 | 70 | 17 | 12 | 4 | 1 | 0 | 1 | 9 | 14 | 7 | 6 | 0 | B | 19 | 18 | 7 |
| HE minus | 18 | 70 | 16 | 7 | 4 | 1 | 1 | 1 | 9 | 14 | 7 | 6 | 1 | B | 19 | 18 | 7 |
| HE minus | 18 | 69-70 | 26f | 16 | 4 | 1 | 2 | 1 | 9 | 14 | 7 | 6 | 2 | B | 19 | 18 | 7 |
| HE minus | 18 | 69-70 | 26r | 19 | 4 | 1 | 3 | 2 | 9 | 14 | 7 | 7 | 2 | B | 19 | 18 | 7 |
| HE minus | 18 | 70 | 18f | 11 | 4 | 1 | 4 | 2 | 9 | 14 | 7 | 7 | 0 | B | 19 | 18 | 7 |
| HE minus | 18 | 70 | 18r | 3 | 4 | 1 | 5 | 2 | 9 | 14 | 7 | 7 | 1 | B | 19 | 18 | 7 |
| HE minus | 18 | 69-70 | 28f | 6 | 4 | 2 | 0 | 3 | 9 | 15 | 8 | 2 | 0 | B | 19 | 18 | 8 |
| HE minus | 18 | 69-70 | 28m | 18 | 4 | 2 | 1 | 3 | 9 | 15 | 8 | 2 | 1 | B | 19 | 18 | 8 |
| HE minus | 18 | 69-70 | 29f | 15 | 4 | 2 | 2 | 3 | 9 | 15 | 8 | 2 | 2 | B | 19 | 18 | 8 |
| HE minus | 18 | 69-70 | 28r | 14 | 4 | 2 | 3 | 4 | 9 | 15 | 8 | 3 | 2 | B | 19 | 18 | 8 |
| HE minus | 18 | 69-70 | 24r | 5 | 4 | 2 | 4 | 4 | 9 | 15 | 8 | 3 | 0 | B | 19 | 18 | 8 |
| HE minus | 18 | 69-70 | 24f | 2 | 4 | 2 | 5 | 4 | 9 | 15 | 8 | 3 | 1 | B | 19 | 18 | 8 |
| HE minus | 18 | 70 | 19r | 1 | 4 | 3 | 0 | 5 | 9 | 15 | 8 | 4 | 0 | T | 19 | 18 | 8 |
| HE minus | 18 | 70 | 20f | 9 | 4 | 3 | 1 | 5 | 9 | 15 | 8 | 4 | 1 | T | 19 | 18 | 8 |
| HE minus | 18 | 69-70 | 22r | 17 | 4 | 3 | 2 | 5 | 9 | 15 | 8 | 4 | 2 | T | 19 | 18 | 8 |
| HE minus | 18 | 69-70 | 19f | 4 | 4 | 3 | 3 | 6 | 9 | 15 | 8 | 5 | 2 | T | 19 | 18 | 8 |
| HE minus | 18 | 70 | 20r | 8 | 4 | 3 | 4 | 6 | 9 | 15 | 8 | 5 | 0 | T | 19 | 18 | 8 |
| HE minus | 18 | 69-70 | 22f | 13 | 4 | 3 | 5 | 6 | 9 | 15 | 8 | 5 | 1 | T | 19 | 18 | 8 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |
| HB plus | 18 | 67 | 16r | 2 | 4 | 1 | 0 | 1 | 9 | 17 | 10 | 0 | 0 | T | 19 | 18 | 10 |

| | | | | | | | | | | | | | | | | | |
|---------|----|-----------|-------|----|------|-----|------|-------|-------|-----|------|-----|------|-------|-----|-----|----|
| HB plus | 18 | 67 | 15r | 7 | 4 | 1 | 1 | 1 | 9 | 17 | 10 | 0 | 1 | T | 19 | 18 | 10 |
| HB plus | 18 | 67 | 14 | 6 | 4 | 1 | 2 | 1 | 9 | 17 | 10 | 0 | 2 | T | 19 | 18 | 10 |
| HB plus | 18 | 67 | 16f | 3 | 4 | 1 | 3 | 2 | 9 | 17 | 10 | 1 | 2 | T | 19 | 18 | 10 |
| HB plus | 18 | 67 | 15f | 1 | 4 | 1 | 4 | 2 | 9 | 17 | 10 | 1 | 0 | T | 19 | 18 | 10 |
| HB plus | 18 | 67 | 13 | 5 | 4 | 1 | 5 | 2 | 9 | 17 | 10 | 1 | 1 | T | 19 | 18 | 10 |
| HB plus | 18 | 67 | 10 | 11 | 4 | 2 | 0 | 3 | 9 | 16 | 9 | 1 | 0 | T | 19 | 18 | 9 |
| HB plus | 18 | 67 | 6 | 15 | 4 | 2 | 1 | 3 | 9 | 16 | 9 | 1 | 1 | T | 19 | 18 | 9 |
| HB plus | 18 | 67 | 2 | 18 | 4 | 2 | 2 | 3 | 9 | 16 | 9 | 1 | 2 | T | 19 | 18 | 9 |
| HB plus | 18 | 67 | 12 | 4 | 4 | 2 | 3 | 4 | 9 | 16 | 9 | 0 | 2 | B | 19 | 18 | 9 |
| HB plus | 18 | 67 | 8 | 8 | 4 | 2 | 4 | 4 | 9 | 16 | 9 | 0 | 0 | B | 19 | 18 | 9 |
| HB plus | 18 | 67 | 4 | 13 | 4 | 2 | 5 | 4 | 9 | 16 | 9 | 0 | 1 | B | 19 | 18 | 9 |
| HB plus | 18 | 67 | 11 | 12 | 4 | 3 | 0 | 5 | 9 | 16 | 9 | 1 | 0 | B | 19 | 18 | 9 |
| HB plus | 18 | 67 | 7 | 16 | 4 | 3 | 1 | 5 | 9 | 16 | 9 | 1 | 1 | B | 19 | 18 | 9 |
| HB plus | 18 | 67 | 3 | 19 | 4 | 3 | 2 | 5 | 9 | 16 | 9 | 1 | 2 | B | 19 | 18 | 9 |
| HB plus | 18 | 67 | 9 | 9 | 4 | 3 | 3 | 6 | 9 | 16 | 9 | 0 | 2 | T | 19 | 18 | 9 |
| HB plus | 18 | 67 | 5 | 14 | 4 | 3 | 4 | 6 | 9 | 16 | 9 | 0 | 0 | T | 19 | 18 | 9 |
| HB plus | 18 | 67 | 1 | 17 | 4 | 3 | 5 | 6 | 9 | 16 | 9 | 0 | 1 | T | 19 | 18 | 9 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |
| HB plus | 18 | 68 | 16r | 3 | 3 | 1 | 0 | 1 | 9 | 17 | 10 | 2 | 0 | T | 19 | 18 | 10 |
| HB plus | 18 | 68 | 15r | 17 | 3 | 1 | 1 | 1 | 9 | 17 | 10 | 2 | 1 | T | 19 | 18 | 10 |
| HB plus | 18 | 68 | 14 | 19 | 3 | 1 | 2 | 1 | 9 | 17 | 10 | 2 | 2 | T | 19 | 18 | 10 |
| HB plus | 18 | 68 | 16f | 1 | 3 | 1 | 3 | 2 | 9 | 17 | 10 | 3 | 2 | T | 19 | 18 | 10 |
| HB plus | 18 | 68 | 15f | 18 | 3 | 1 | 4 | 2 | 9 | 17 | 10 | 3 | 0 | T | 19 | 18 | 10 |
| HB plus | 18 | 68 | 13 | 13 | 3 | 1 | 5 | 2 | 9 | 17 | 10 | 3 | 1 | T | 19 | 18 | 10 |
| HB plus | 18 | 68 | 10 | 16 | 3 | 2 | 0 | 3 | 9 | 16 | 9 | 3 | 0 | T | 19 | 18 | 9 |
| HB plus | 18 | 68 | 6 | 12 | 3 | 2 | 1 | 3 | 9 | 16 | 9 | 3 | 1 | T | 19 | 18 | 9 |
| HB plus | 18 | 68 | 2 | 7 | 3 | 2 | 2 | 3 | 9 | 16 | 9 | 3 | 2 | T | 19 | 18 | 9 |
| HB plus | 18 | 68 | 12 | 14 | 3 | 2 | 3 | 4 | 9 | 16 | 9 | 2 | 2 | B | 19 | 18 | 9 |
| HB plus | 18 | 68 | 8 | 9 | 3 | 2 | 4 | 4 | 9 | 16 | 9 | 2 | 0 | B | 19 | 18 | 9 |
| HB plus | 18 | 68 | 4 | 5 | 3 | 2 | 5 | 4 | 9 | 16 | 9 | 2 | 1 | B | 19 | 18 | 9 |
| HB plus | 18 | 68 | 11 | 15 | 3 | 3 | 0 | 5 | 9 | 16 | 9 | 3 | 0 | B | 19 | 18 | 9 |
| HB plus | 18 | 68 | 7 | 11 | 3 | 3 | 1 | 5 | 9 | 16 | 9 | 3 | 1 | B | 19 | 18 | 9 |
| HB plus | 18 | 68 | 3 | 6 | 3 | 3 | 2 | 5 | 9 | 16 | 9 | 3 | 2 | B | 19 | 18 | 9 |
| HB plus | 18 | 68 | 9 | 8 | 3 | 3 | 3 | 6 | 9 | 16 | 9 | 2 | 2 | T | 19 | 18 | 9 |
| HB plus | 18 | 68 | 5 | 4 | 3 | 3 | 4 | 6 | 9 | 16 | 9 | 2 | 0 | T | 19 | 18 | 9 |
| HB plus | 18 | 68 | 1 | 2 | 3 | 3 | 5 | 6 | 9 | 16 | 9 | 2 | 1 | T | 19 | 18 | 9 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |
| HB plus | 18 | 69 | 16r | 17 | 2 | 1 | 0 | 1 | 9 | 17 | 10 | 0 | 0 | B | 19 | 18 | 10 |
| HB plus | 18 | 69 | 15r | 3 | 2 | 1 | 1 | 1 | 9 | 17 | 10 | 0 | 1 | B | 19 | 18 | 10 |
| HB plus | 18 | 69 | 14 | 1 | 2 | 1 | 2 | 1 | 9 | 17 | 10 | 0 | 2 | B | 19 | 18 | 10 |
| HB plus | 18 | 69 | 16f | 19 | 2 | 1 | 3 | 2 | 9 | 17 | 10 | 1 | 2 | B | 19 | 18 | 10 |
| HB plus | 18 | 69 | 15f | 2 | 2 | 1 | 4 | 2 | 9 | 17 | 10 | 1 | 0 | B | 19 | 18 | 10 |
| HB plus | 18 | 69 | 13 | 7 | 2 | 1 | 5 | 2 | 9 | 17 | 10 | 1 | 1 | B | 19 | 18 | 10 |
| HB plus | 18 | 69 | 10 | 4 | 2 | 2 | 0 | 3 | 9 | 16 | 9 | 5 | 0 | T | 19 | 18 | 9 |
| HB plus | 18 | 69 | 6 | 8 | 2 | 2 | 1 | 3 | 9 | 16 | 9 | 5 | 1 | T | 19 | 18 | 9 |
| HB plus | 18 | 69 | 2 | 13 | 2 | 2 | 2 | 3 | 9 | 16 | 9 | 5 | 2 | T | 19 | 18 | 9 |
| HB plus | 18 | 69 | 12 | 6 | 2 | 2 | 3 | 4 | 9 | 16 | 9 | 4 | 2 | B | 19 | 18 | 9 |
| HB plus | 18 | 69 | 8 | 11 | 2 | 2 | 4 | 4 | 9 | 16 | 9 | 4 | 0 | B | 19 | 18 | 9 |
| HB plus | 18 | 69 | 4 | 15 | 2 | 2 | 5 | 4 | 9 | 16 | 9 | 4 | 1 | B | 19 | 18 | 9 |
| HB plus | 18 | 69 | 11 | 5 | 2 | 3 | 0 | 5 | 9 | 16 | 9 | 5 | 0 | B | 19 | 18 | 9 |
| HB plus | 18 | 69 | 7 | 9 | 2 | 3 | 1 | 5 | 9 | 16 | 9 | 5 | 1 | B | 19 | 18 | 9 |

| | | | | | | | | | | | | | | | | | |
|---------|----|-----------|-------|----|------|-----|------|-------|-------|-----|------|-----|------|-------|-----|-----|----|
| HB plus | 18 | 69 | 3 | 14 | 2 | 3 | 2 | 5 | 9 | 16 | 9 | 5 | 2 | B | 19 | 18 | 9 |
| HB plus | 18 | 69 | 9 | 12 | 2 | 3 | 3 | 6 | 9 | 16 | 9 | 4 | 2 | T | 19 | 18 | 9 |
| HB plus | 18 | 69 | 5 | 16 | 2 | 3 | 4 | 6 | 9 | 16 | 9 | 4 | 0 | T | 19 | 18 | 9 |
| HB plus | 18 | 69 | 1 | 18 | 2 | 3 | 5 | 6 | 9 | 16 | 9 | 4 | 1 | T | 19 | 18 | 9 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |
| HB plus | 18 | 70 | 16r | 18 | 1 | 1 | 0 | 1 | 9 | 17 | 10 | 2 | 0 | B | 19 | 18 | 10 |
| HB plus | 18 | 70 | 15r | 13 | 1 | 1 | 1 | 1 | 9 | 17 | 10 | 2 | 1 | B | 19 | 18 | 10 |
| HB plus | 18 | 70 | 14 | 14 | 1 | 1 | 2 | 1 | 9 | 17 | 10 | 2 | 2 | B | 19 | 18 | 10 |
| HB plus | 18 | 70 | 16f | 17 | 1 | 1 | 3 | 2 | 9 | 17 | 10 | 3 | 2 | B | 19 | 18 | 10 |
| HB plus | 18 | 70 | 15f | 19 | 1 | 1 | 4 | 2 | 9 | 17 | 10 | 3 | 0 | B | 19 | 18 | 10 |
| HB plus | 18 | 70 | 13 | 15 | 1 | 1 | 5 | 2 | 9 | 17 | 10 | 3 | 1 | B | 19 | 18 | 10 |
| HB plus | 18 | 70 | 10 | 9 | 1 | 2 | 0 | 3 | 9 | 16 | 9 | 7 | 0 | T | 19 | 18 | 9 |
| HB plus | 18 | 70 | 6 | 5 | 1 | 2 | 1 | 3 | 9 | 16 | 9 | 7 | 1 | T | 19 | 18 | 9 |
| HB plus | 18 | 70 | 2 | 2 | 1 | 2 | 2 | 3 | 9 | 16 | 9 | 7 | 2 | T | 19 | 18 | 9 |
| HB plus | 18 | 70 | 12 | 16 | 1 | 2 | 3 | 4 | 9 | 16 | 9 | 6 | 2 | B | 19 | 18 | 9 |
| HB plus | 18 | 70 | 8 | 12 | 1 | 2 | 4 | 4 | 9 | 16 | 9 | 6 | 0 | B | 19 | 18 | 9 |
| HB plus | 18 | 70 | 4 | 7 | 1 | 2 | 5 | 4 | 9 | 16 | 9 | 6 | 1 | B | 19 | 18 | 9 |
| HB plus | 18 | 70 | 11 | 8 | 1 | 3 | 0 | 5 | 9 | 16 | 9 | 7 | 0 | B | 19 | 18 | 9 |
| HB plus | 18 | 70 | 7 | 4 | 1 | 3 | 1 | 5 | 9 | 16 | 9 | 7 | 1 | B | 19 | 18 | 9 |
| HB plus | 18 | 70 | 3 | 1 | 1 | 3 | 2 | 5 | 9 | 16 | 9 | 7 | 2 | B | 19 | 18 | 9 |
| HB plus | 18 | 70 | 9 | 11 | 1 | 3 | 3 | 6 | 9 | 16 | 9 | 6 | 2 | T | 19 | 18 | 9 |
| HB plus | 18 | 70 | 5 | 6 | 1 | 3 | 4 | 6 | 9 | 16 | 9 | 6 | 0 | T | 19 | 18 | 9 |
| HB plus | 18 | 70 | 1 | 3 | 1 | 3 | 5 | 6 | 9 | 16 | 9 | 6 | 1 | T | 19 | 18 | 9 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |
| HE plus | 18 | 67 | 17 | 12 | 4 | 1 | 0 | 1 | 9 | 17 | 10 | 4 | 0 | T | 19 | 18 | 10 |
| HE plus | 18 | 67 | 16 | 7 | 4 | 1 | 1 | 1 | 9 | 17 | 10 | 4 | 1 | T | 19 | 18 | 10 |
| HE plus | 18 | 67-68 | 26f | 16 | 4 | 1 | 2 | 1 | 9 | 17 | 10 | 4 | 2 | T | 19 | 18 | 10 |
| HE plus | 18 | 67-68 | 26r | 19 | 4 | 1 | 3 | 2 | 9 | 17 | 10 | 5 | 2 | T | 19 | 18 | 10 |
| HE plus | 18 | 67 | 18f | 11 | 4 | 1 | 4 | 2 | 9 | 17 | 10 | 5 | 0 | T | 19 | 18 | 10 |
| HE plus | 18 | 67 | 18r | 3 | 4 | 1 | 5 | 2 | 9 | 17 | 10 | 5 | 1 | T | 19 | 18 | 10 |
| HE plus | 18 | 67-68 | 28f | 6 | 4 | 2 | 0 | 3 | 9 | 18 | 11 | 0 | 0 | B | 19 | 18 | 11 |
| HE plus | 18 | 67-68 | 28m | 18 | 4 | 2 | 1 | 3 | 9 | 18 | 11 | 0 | 1 | B | 19 | 18 | 11 |
| HE plus | 18 | 67-68 | 29f | 15 | 4 | 2 | 2 | 3 | 9 | 18 | 11 | 0 | 2 | B | 19 | 18 | 11 |
| HE plus | 18 | 67-68 | 28r | 14 | 4 | 2 | 3 | 4 | 9 | 18 | 11 | 1 | 2 | B | 19 | 18 | 11 |
| HE plus | 18 | 67-68 | 24r | 5 | 4 | 2 | 4 | 4 | 9 | 18 | 11 | 1 | 0 | B | 19 | 18 | 11 |
| HE plus | 18 | 67-68 | 24f | 2 | 4 | 2 | 5 | 4 | 9 | 18 | 11 | 1 | 1 | B | 19 | 18 | 11 |
| HE plus | 18 | 67 | 19r | 1 | 4 | 3 | 0 | 5 | 9 | 18 | 11 | 0 | 0 | T | 19 | 18 | 11 |
| HE plus | 18 | 67 | 20f | 9 | 4 | 3 | 1 | 5 | 9 | 18 | 11 | 0 | 1 | T | 19 | 18 | 11 |
| HE plus | 18 | 67-68 | 22r | 17 | 4 | 3 | 2 | 5 | 9 | 18 | 11 | 0 | 2 | T | 19 | 18 | 11 |
| HE plus | 18 | 67-68 | 19f | 4 | 4 | 3 | 3 | 6 | 9 | 18 | 11 | 1 | 2 | T | 19 | 18 | 11 |
| HE plus | 18 | 67 | 20r | 8 | 4 | 3 | 4 | 6 | 9 | 18 | 11 | 1 | 0 | T | 19 | 18 | 11 |
| HE plus | 18 | 67-68 | 22f | 13 | 4 | 3 | 5 | 6 | 9 | 18 | 11 | 1 | 1 | T | 19 | 18 | 11 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |
| HE plus | 18 | 68 | 17 | 12 | 3 | 1 | 0 | 1 | 9 | 17 | 10 | 6 | 0 | T | 19 | 18 | 10 |
| HE plus | 18 | 68 | 16 | 16 | 3 | 1 | 1 | 1 | 9 | 17 | 10 | 6 | 1 | T | 19 | 18 | 10 |
| HE plus | 18 | 67-68 | 25f | 7 | 3 | 1 | 2 | 1 | 9 | 17 | 10 | 6 | 2 | T | 19 | 18 | 10 |
| HE plus | 18 | 67-68 | 25r | 3 | 3 | 1 | 3 | 2 | 9 | 17 | 10 | 7 | 2 | T | 19 | 18 | 10 |
| HE plus | 18 | 68 | 18f | 11 | 3 | 1 | 4 | 2 | 9 | 17 | 10 | 7 | 0 | T | 19 | 18 | 10 |
| HE plus | 18 | 68 | 18r | 19 | 3 | 1 | 5 | 2 | 9 | 17 | 10 | 7 | 1 | T | 19 | 18 | 10 |
| HE plus | 18 | 67-68 | 27f | 6 | 3 | 2 | 0 | 3 | 9 | 18 | 11 | 4 | 0 | B | 19 | 18 | 11 |
| HE plus | 18 | 67-68 | 27m | 2 | 3 | 2 | 1 | 3 | 9 | 18 | 11 | 4 | 1 | B | 19 | 18 | 11 |

| | | | | | | | | | | | | | | | | | |
|---------|----|-----------|-------|----|------|-----|------|-------|-------|-----|------|-----|------|-------|-----|-----|----|
| HE plus | 18 | 67-68 | 29m | 15 | 3 | 2 | 2 | 3 | 9 | 18 | 11 | 4 | 2 | B | 19 | 18 | 11 |
| HE plus | 18 | 67-68 | 23f | 18 | 3 | 2 | 3 | 4 | 9 | 18 | 11 | 5 | 2 | B | 19 | 18 | 11 |
| HE plus | 18 | 67-68 | 27r | 5 | 3 | 2 | 4 | 4 | 9 | 18 | 11 | 5 | 0 | B | 19 | 18 | 11 |
| HE plus | 18 | 67-68 | 23r | 14 | 3 | 2 | 5 | 4 | 9 | 18 | 11 | 5 | 1 | B | 19 | 18 | 11 |
| HE plus | 18 | 68 | 19r | 17 | 3 | 3 | 0 | 5 | 9 | 18 | 11 | 2 | 0 | T | 19 | 18 | 11 |
| HE plus | 18 | 68 | 20f | 9 | 3 | 3 | 1 | 5 | 9 | 18 | 11 | 2 | 1 | T | 19 | 18 | 11 |
| HE plus | 18 | 67-68 | 21r | 1 | 3 | 3 | 2 | 5 | 9 | 18 | 11 | 2 | 2 | T | 19 | 18 | 11 |
| HE plus | 18 | 67-68 | 21f | 4 | 3 | 3 | 3 | 6 | 9 | 18 | 11 | 3 | 2 | T | 19 | 18 | 11 |
| HE plus | 18 | 68 | 19f | 13 | 3 | 3 | 4 | 6 | 9 | 18 | 11 | 3 | 0 | T | 19 | 18 | 11 |
| HE plus | 18 | 67-68 | 20r | 8 | 3 | 3 | 5 | 6 | 9 | 18 | 11 | 3 | 1 | T | 19 | 18 | 11 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |
| HE plus | 18 | 69 | 17 | 12 | 2 | 1 | 0 | 1 | 9 | 17 | 10 | 4 | 0 | B | 19 | 18 | 10 |
| HE plus | 18 | 69 | 16 | 7 | 2 | 1 | 1 | 1 | 9 | 17 | 10 | 4 | 1 | B | 19 | 18 | 10 |
| HE plus | 18 | 69-70 | 25f | 16 | 2 | 1 | 2 | 1 | 9 | 17 | 10 | 4 | 2 | B | 19 | 18 | 10 |
| HE plus | 18 | 69-70 | 25r | 19 | 2 | 1 | 3 | 2 | 9 | 17 | 10 | 5 | 2 | B | 19 | 18 | 10 |
| HE plus | 18 | 69 | 18f | 11 | 2 | 1 | 4 | 2 | 9 | 17 | 10 | 5 | 0 | B | 19 | 18 | 10 |
| HE plus | 18 | 69 | 18r | 3 | 2 | 1 | 5 | 2 | 9 | 17 | 10 | 5 | 1 | B | 19 | 18 | 10 |
| HE plus | 18 | 69-70 | 27f | 15 | 2 | 2 | 0 | 3 | 9 | 18 | 11 | 6 | 0 | B | 19 | 18 | 11 |
| HE plus | 18 | 69-70 | 27m | 18 | 2 | 2 | 1 | 3 | 9 | 18 | 11 | 6 | 1 | B | 19 | 18 | 11 |
| HE plus | 18 | 69-70 | 29m | 6 | 2 | 2 | 2 | 3 | 9 | 18 | 11 | 6 | 2 | B | 19 | 18 | 11 |
| HE plus | 18 | 69-70 | 23f | 2 | 2 | 2 | 3 | 4 | 9 | 18 | 11 | 7 | 2 | B | 19 | 18 | 11 |
| HE plus | 18 | 69-70 | 27r | 14 | 2 | 2 | 4 | 4 | 9 | 18 | 11 | 7 | 0 | B | 19 | 18 | 11 |
| HE plus | 18 | 69-70 | 23r | 5 | 2 | 2 | 5 | 4 | 9 | 18 | 11 | 7 | 1 | B | 19 | 18 | 11 |
| HE plus | 18 | 69 | 19r | 1 | 2 | 3 | 0 | 5 | 9 | 18 | 11 | 6 | 0 | T | 19 | 18 | 11 |
| HE plus | 18 | 69 | 20f | 9 | 2 | 3 | 1 | 5 | 9 | 18 | 11 | 6 | 1 | T | 19 | 18 | 11 |
| HE plus | 18 | 69-70 | 21r | 17 | 2 | 3 | 2 | 5 | 9 | 18 | 11 | 6 | 2 | T | 19 | 18 | 11 |
| HE plus | 18 | 69-70 | 21f | 13 | 2 | 3 | 3 | 6 | 9 | 18 | 11 | 7 | 2 | T | 19 | 18 | 11 |
| HE plus | 18 | 69 | 19f | 4 | 2 | 3 | 4 | 6 | 9 | 18 | 11 | 7 | 0 | T | 19 | 18 | 11 |
| HE plus | 18 | 69-70 | 20r | 8 | 2 | 3 | 5 | 6 | 9 | 18 | 11 | 7 | 1 | T | 19 | 18 | 11 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |
| HE plus | 18 | 70 | 17 | 12 | 1 | 1 | 0 | 1 | 9 | 17 | 10 | 6 | 0 | B | 19 | 18 | 10 |
| HE plus | 18 | 70 | 16 | 16 | 1 | 1 | 1 | 1 | 9 | 17 | 10 | 6 | 1 | B | 19 | 18 | 10 |
| HE plus | 18 | 69-70 | 26f | 7 | 1 | 1 | 2 | 1 | 9 | 17 | 10 | 6 | 2 | B | 19 | 18 | 10 |
| HE plus | 18 | 69-70 | 26r | 3 | 1 | 1 | 3 | 2 | 9 | 17 | 10 | 7 | 2 | B | 19 | 18 | 10 |
| HE plus | 18 | 70 | 18f | 11 | 1 | 1 | 4 | 2 | 9 | 17 | 10 | 7 | 0 | B | 19 | 18 | 10 |
| HE plus | 18 | 70 | 18r | 19 | 1 | 1 | 5 | 2 | 9 | 17 | 10 | 7 | 1 | B | 19 | 18 | 10 |
| HE plus | 18 | 69-70 | 28f | 15 | 1 | 2 | 0 | 3 | 9 | 18 | 11 | 2 | 0 | B | 19 | 18 | 11 |
| HE plus | 18 | 69-70 | 28m | 2 | 1 | 2 | 1 | 3 | 9 | 18 | 11 | 2 | 1 | B | 19 | 18 | 11 |
| HE plus | 18 | 69-70 | 29f | 6 | 1 | 2 | 2 | 3 | 9 | 18 | 11 | 2 | 2 | B | 19 | 18 | 11 |
| HE plus | 18 | 69-70 | 28r | 5 | 1 | 2 | 3 | 4 | 9 | 18 | 11 | 3 | 2 | B | 19 | 18 | 11 |
| HE plus | 18 | 69-70 | 24r | 14 | 1 | 2 | 4 | 4 | 9 | 18 | 11 | 3 | 0 | B | 19 | 18 | 11 |
| HE plus | 18 | 69-70 | 24f | 18 | 1 | 2 | 5 | 4 | 9 | 18 | 11 | 3 | 1 | B | 19 | 18 | 11 |
| HE plus | 18 | 70 | 19r | 17 | 1 | 3 | 0 | 5 | 9 | 18 | 11 | 4 | 0 | T | 19 | 18 | 11 |
| HE plus | 18 | 70 | 20f | 9 | 1 | 3 | 1 | 5 | 9 | 18 | 11 | 4 | 1 | T | 19 | 18 | 11 |
| HE plus | 18 | 69-70 | 22r | 4 | 1 | 3 | 2 | 5 | 9 | 18 | 11 | 4 | 2 | T | 19 | 18 | 11 |
| HE plus | 18 | 69-70 | 19f | 13 | 1 | 3 | 3 | 6 | 9 | 18 | 11 | 5 | 2 | T | 19 | 18 | 11 |
| HE plus | 18 | 70 | 20r | 8 | 1 | 3 | 4 | 6 | 9 | 18 | 11 | 5 | 0 | T | 19 | 18 | 11 |
| HE plus | 18 | 69-70 | 22f | 1 | 1 | 3 | 5 | 6 | 9 | 18 | 11 | 5 | 1 | T | 19 | 18 | 11 |
| Det | W | Phi tower | pixel | RM | Card | QIE | Qfib | Crate | Hslot | HTR | Hfib | Hch | FPGA | Dslot | DCC | Dch | |