Test Beam 2002 (July/August)

Goal:

Absolute calibration of wedges with front end electronics

Bandwidth

Spill is 5.2 s on, 11.6 s off (40 MHz substructure)

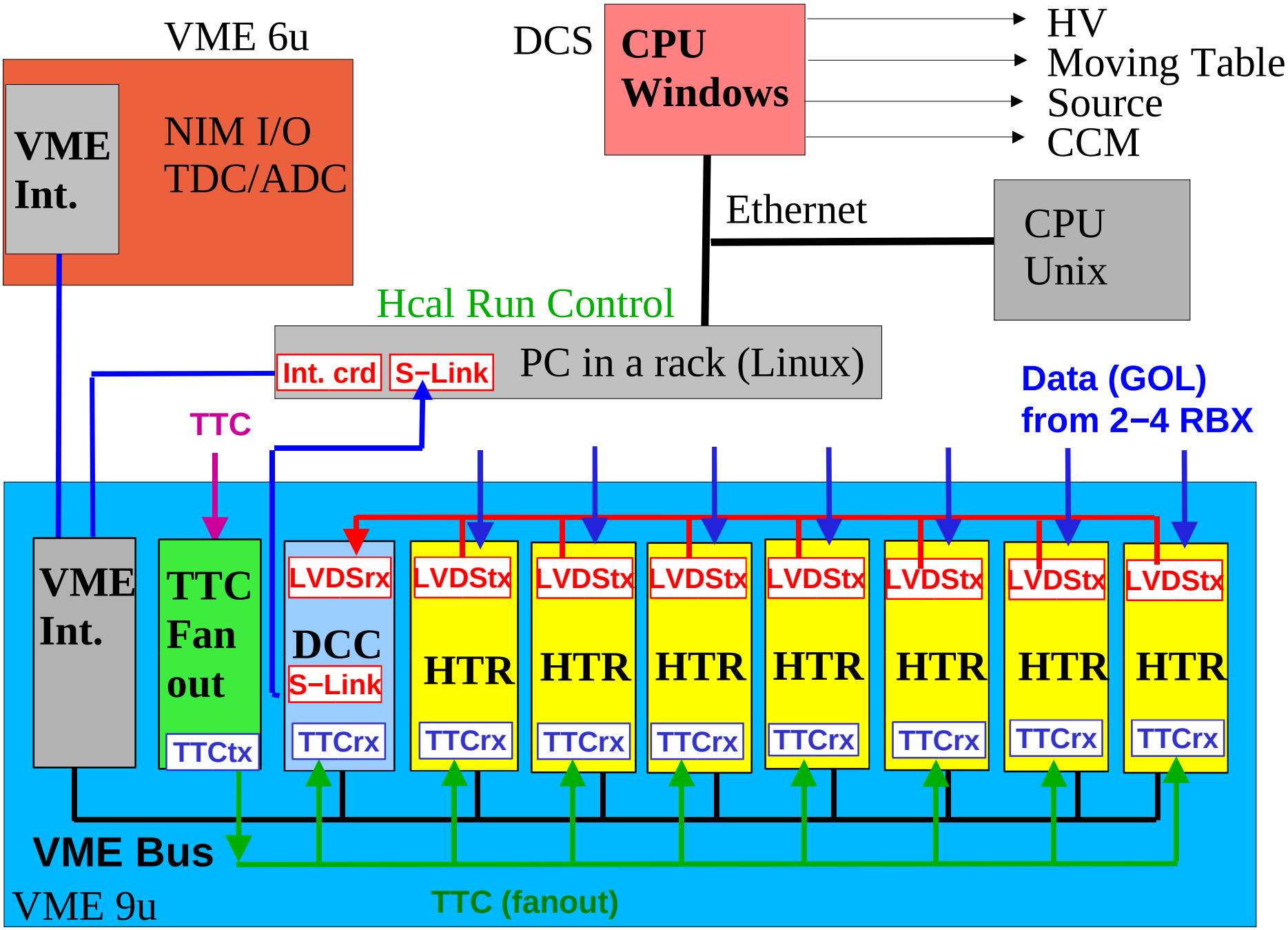
Trigger rate = 5 kHz

(300 ch.)(10 Bytes /ch.)(5000/s)(5 s)= 8 MB/s

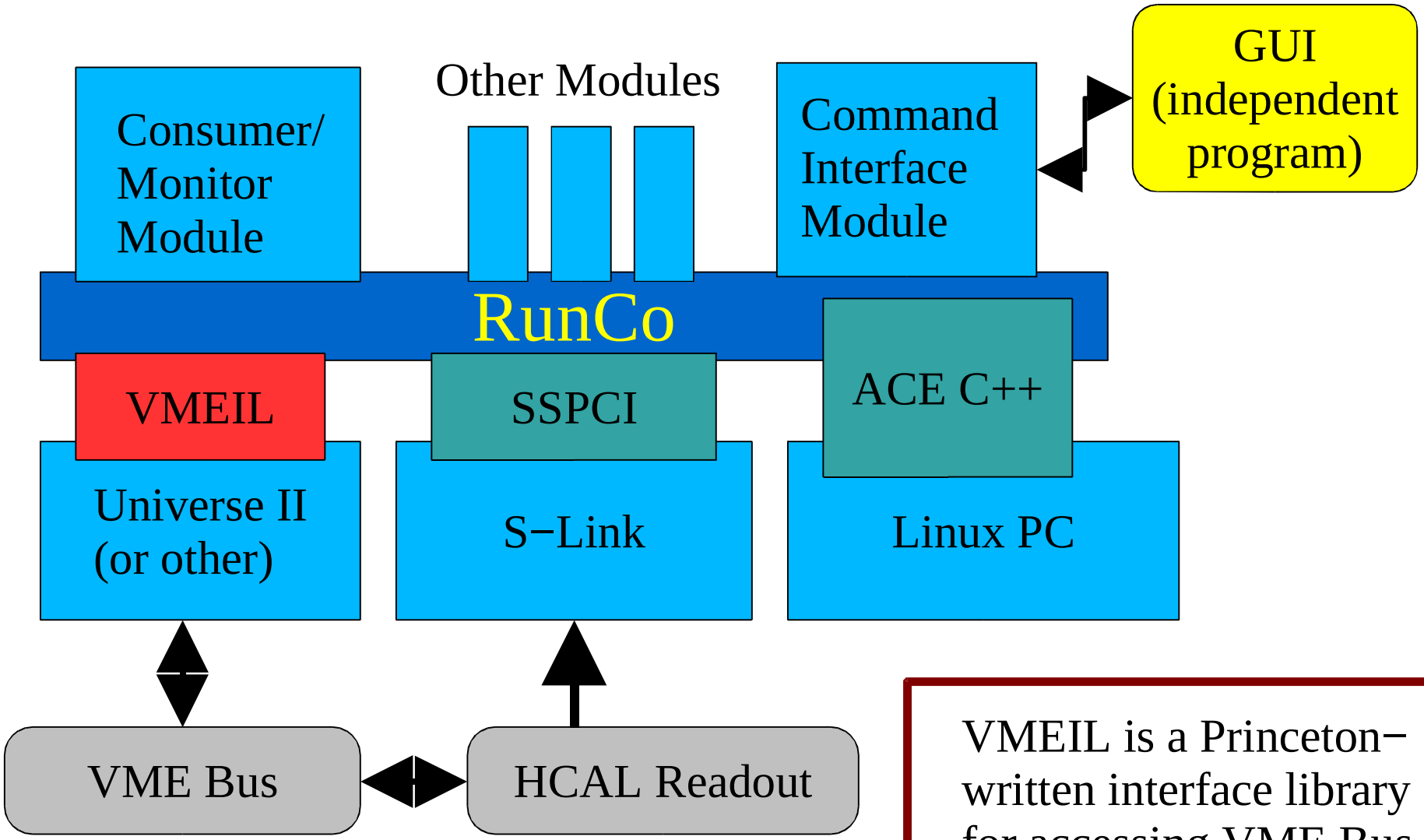
40 MBytes per spill

160 MBytes/m or 1 GByte per hour

CERN 2002 Test Beam



Run Control Software: Modular/Multithreaded
(Princeton)



VMEIL is a Princeton−written interface library for accessing VME Bus

The future scheme

The ACE software package and Hannapel drivers (VMEIL) are standard components connectable to DCS framework via SLIC

Worklist

1) build/test HTR
(2 × 48 ch. or 4 × 24 minimum)
2) build/test 72+ QIE
3) integrate TTC fanout
4) tune up CERN electronics and integrate with HTR/DCC/Slink
5) software for DAQ
6) DCS
source, CCM, HV and table