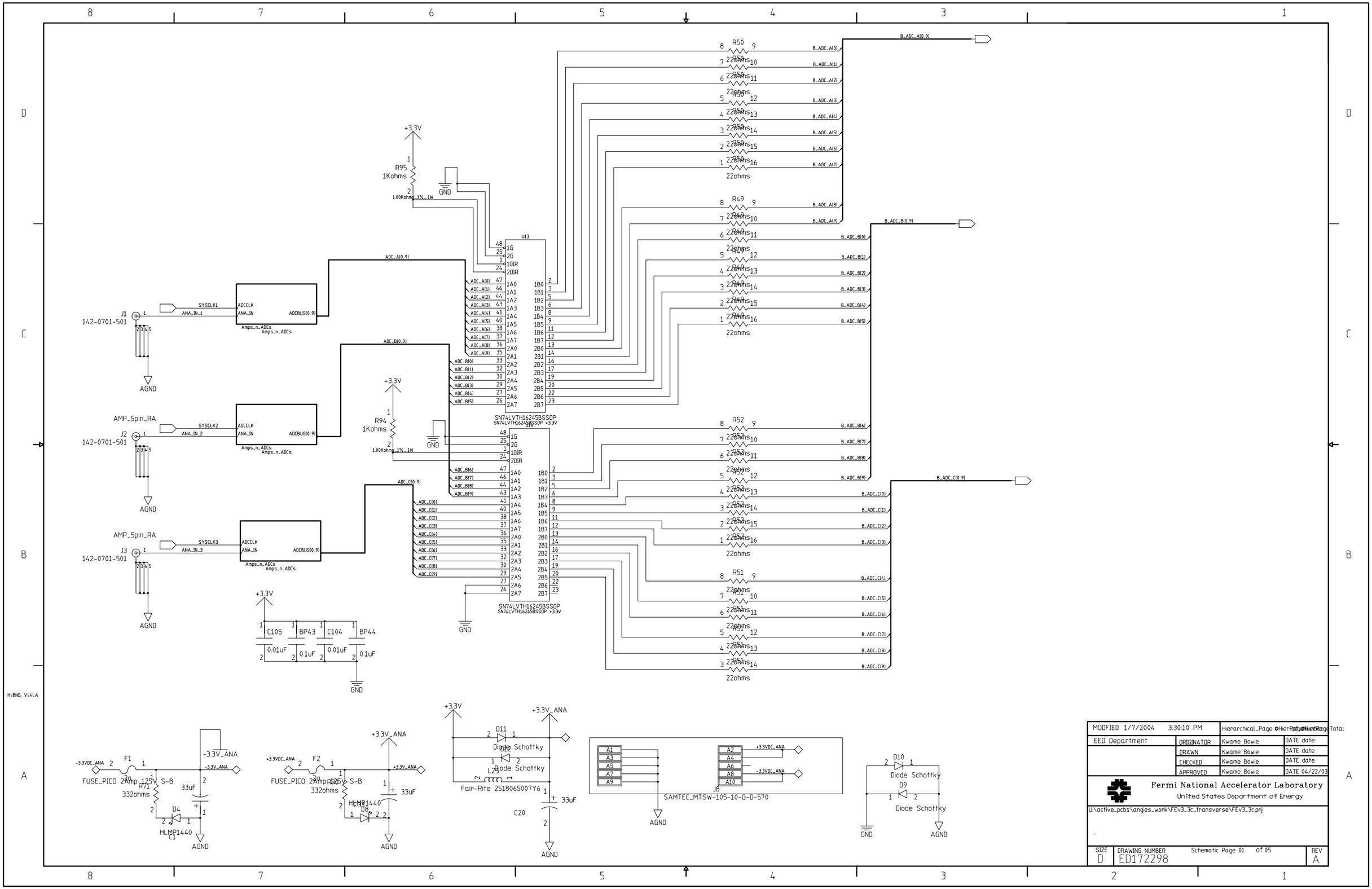

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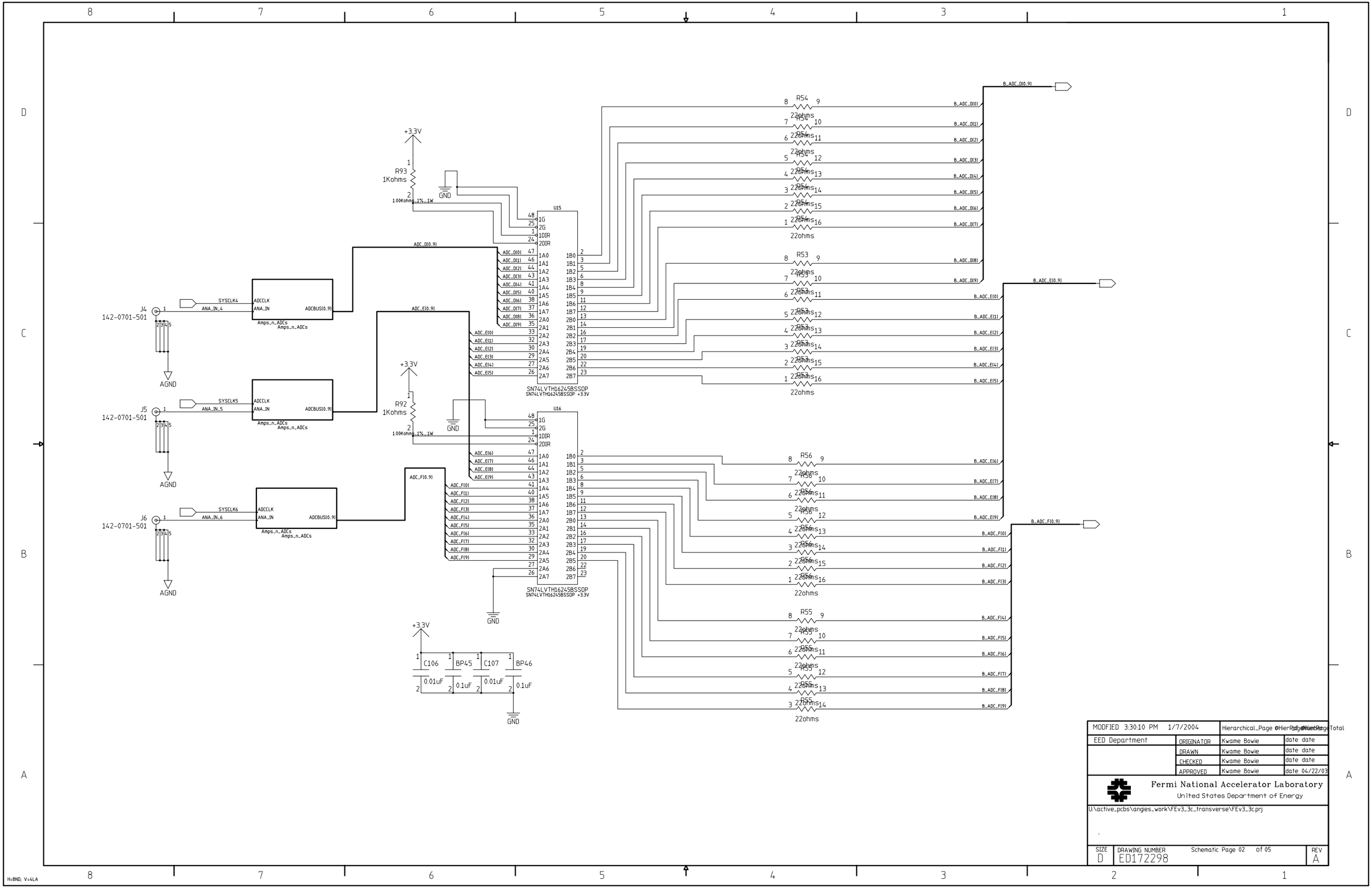

J4
142-0701-501
J5
142-0701-501
J6
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SYSCLK4
SYSCLK5
SYSCLK6
ANA_IN_4
ANA_IN_5
ANA_IN_6
ADCCLK
ANA_IN
ADCBUS(0.9)
Amps_n_ADCs
AGND
+3.3V
R93
1Kohms
R92
1Kohms
1.00Kohms_1%_1W
GND
U15
U16
SN74LVTH16245BSSOP
SN74LVTH16245BSSOP +3.3V
ADC_D(0.9)
ADC_E(0.9)
ADC_F(0.9)
R54
R53
R56
R55
22ohms
C106
BP45
C107
BP46
0.01uF
0.1uF
B_ADC_D(0.9)
B_ADC_E(0.9)
B_ADC_F(0.9)
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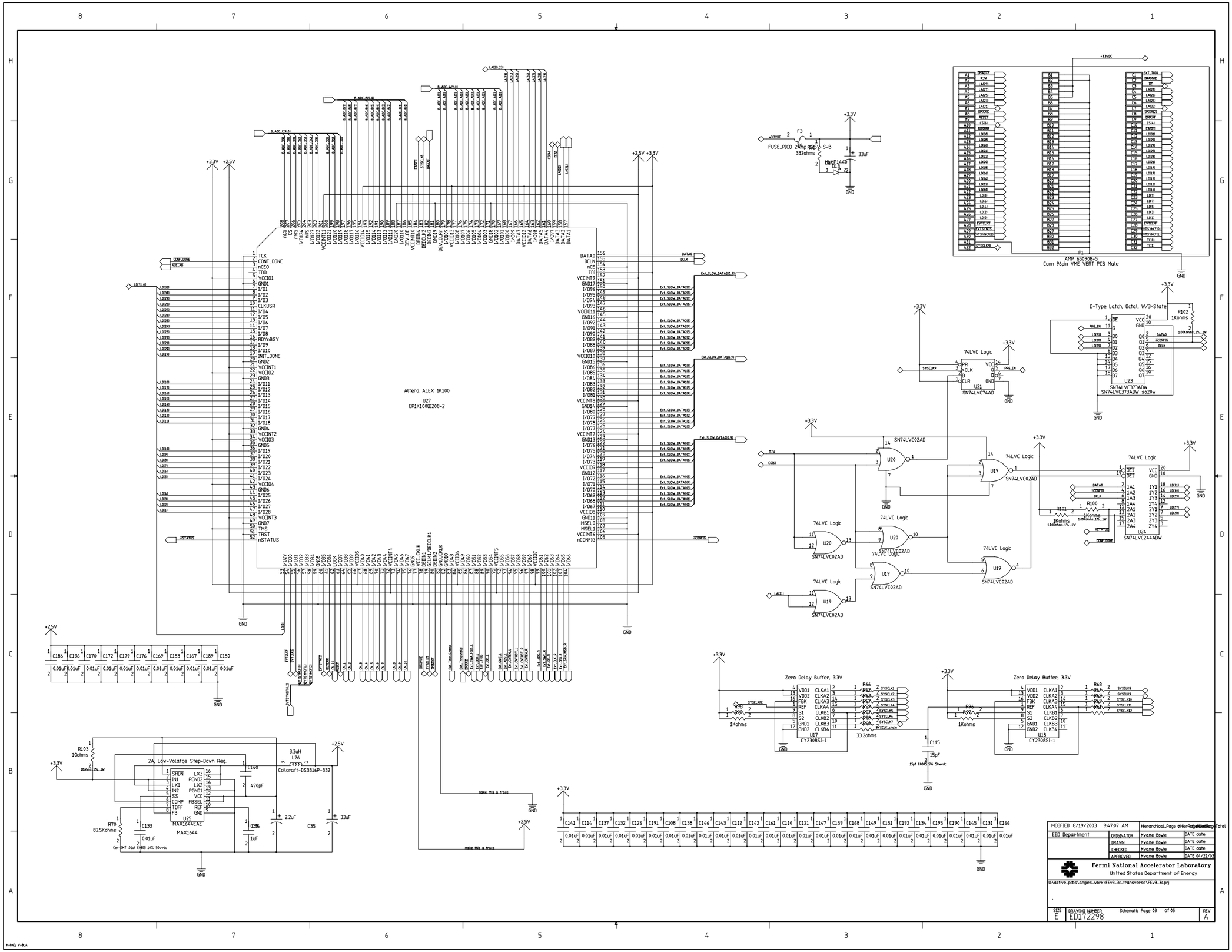

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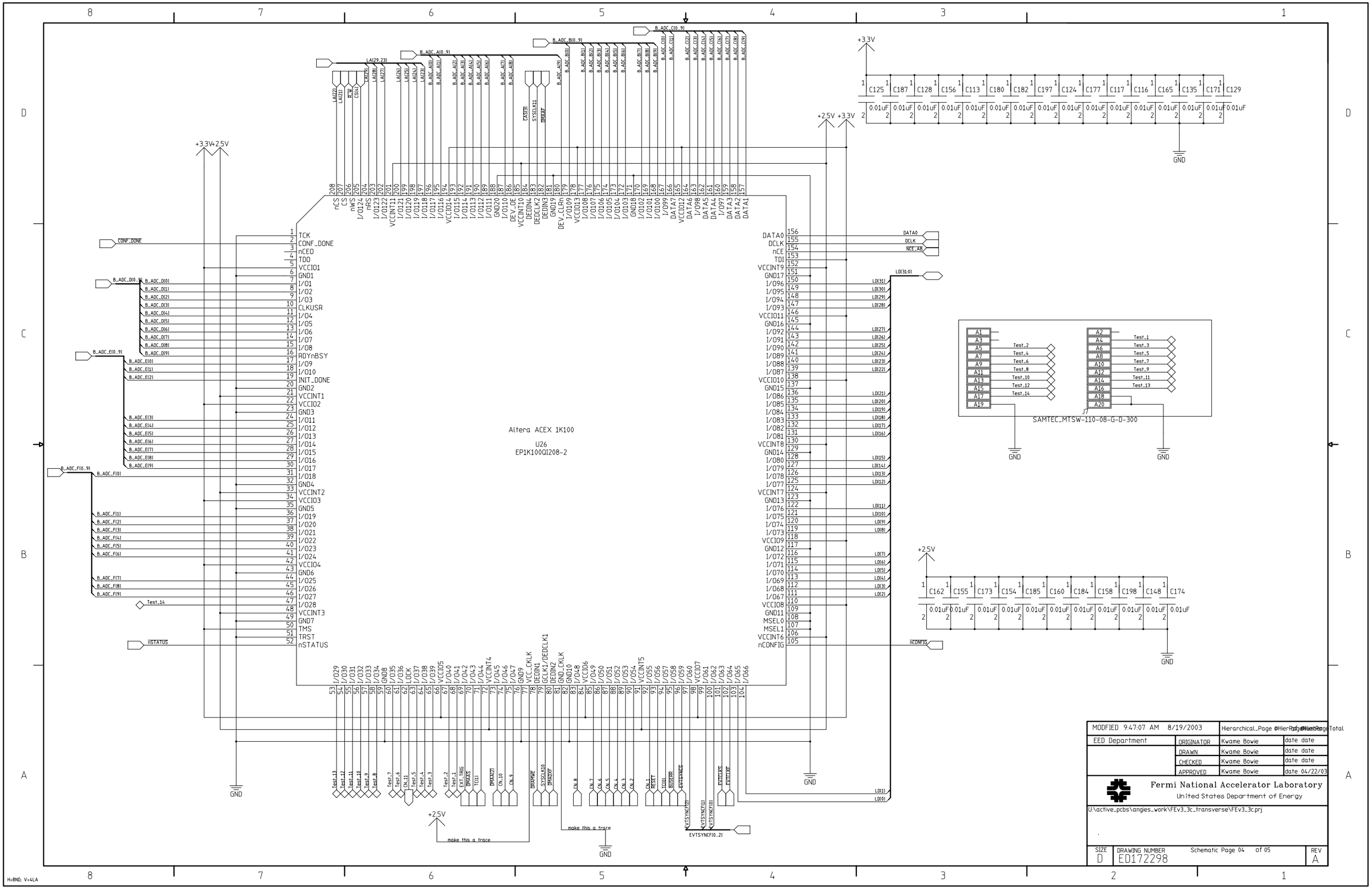

Altera ACEX 1K100
U26
EP1K100QI208-2
SAMTEC_MTSW-110-08-G-D-300
J7
+3.3V
+2.5V
+3.3V+2.5V
GND
make this a trace
EVTSYNCF(0..2)
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REV A
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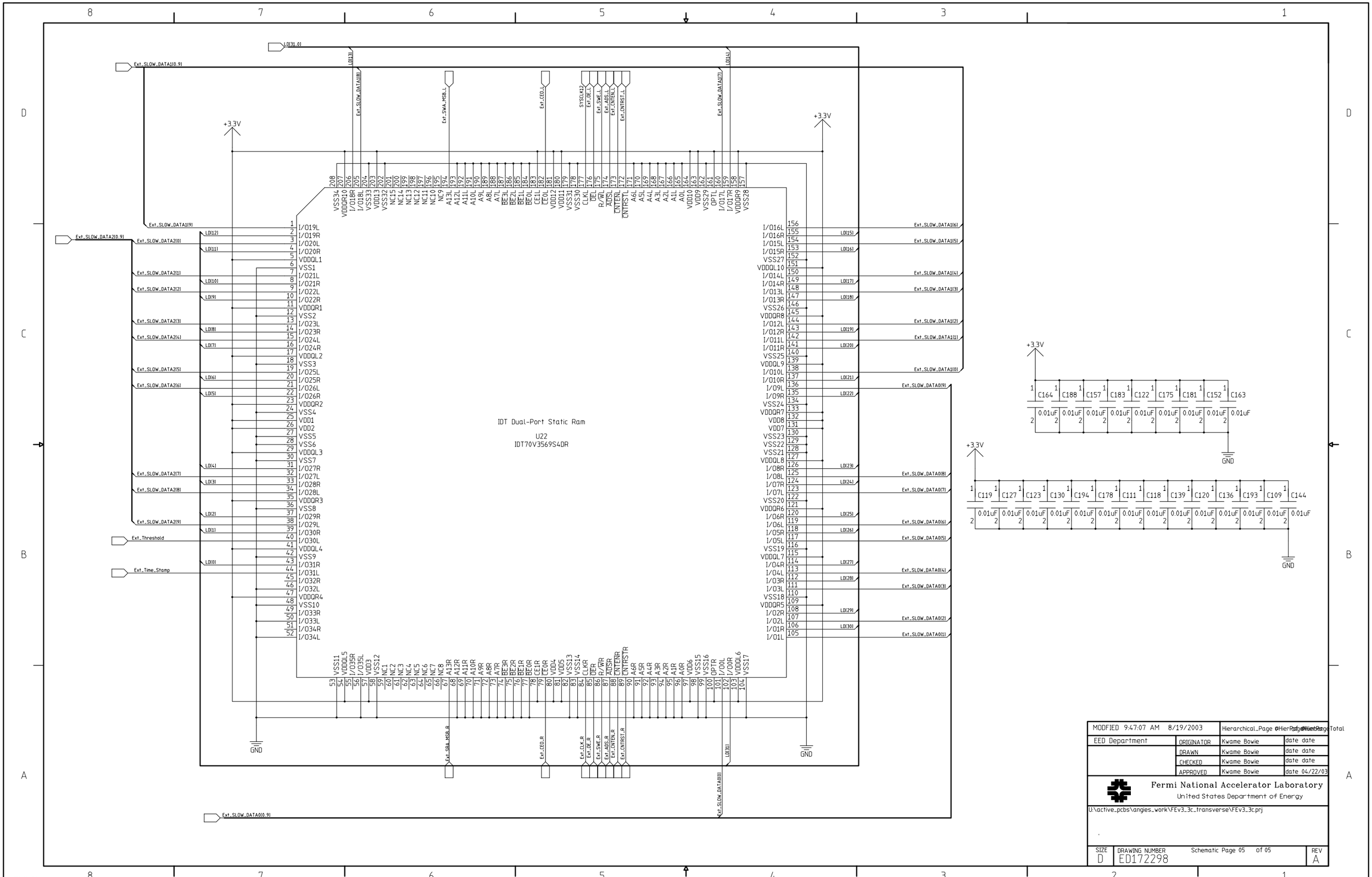
IDT Dual-Port Static Ram
U22
IDT70V3569S4DR
+3.3V
GND
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Ext_SLOW_DATA2[0.9]
Ext_Threshold
Ext_Time_Stamp
Ext_SLOW_DATA0[0.9]
Ext_SWA_MSB_L
Ext_CEO_L
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Ext_SLOW_DATA0[4]
Ext_SLOW_DATA0[3]
Ext_SLOW_DATA0[2]
Ext_SLOW_DATA0[1]
Ext_SLOW_DATA0[0]
Ext_SWA_MSB_R
Ext_CEO_R
Ext_CLK_R
Ext_OE_R
Ext_SWE_R
Ext_ADS_R
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Ext_CNTRST_R
C164 C188 C157 C183 C122 C175 C181 C152 C163
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REV A
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