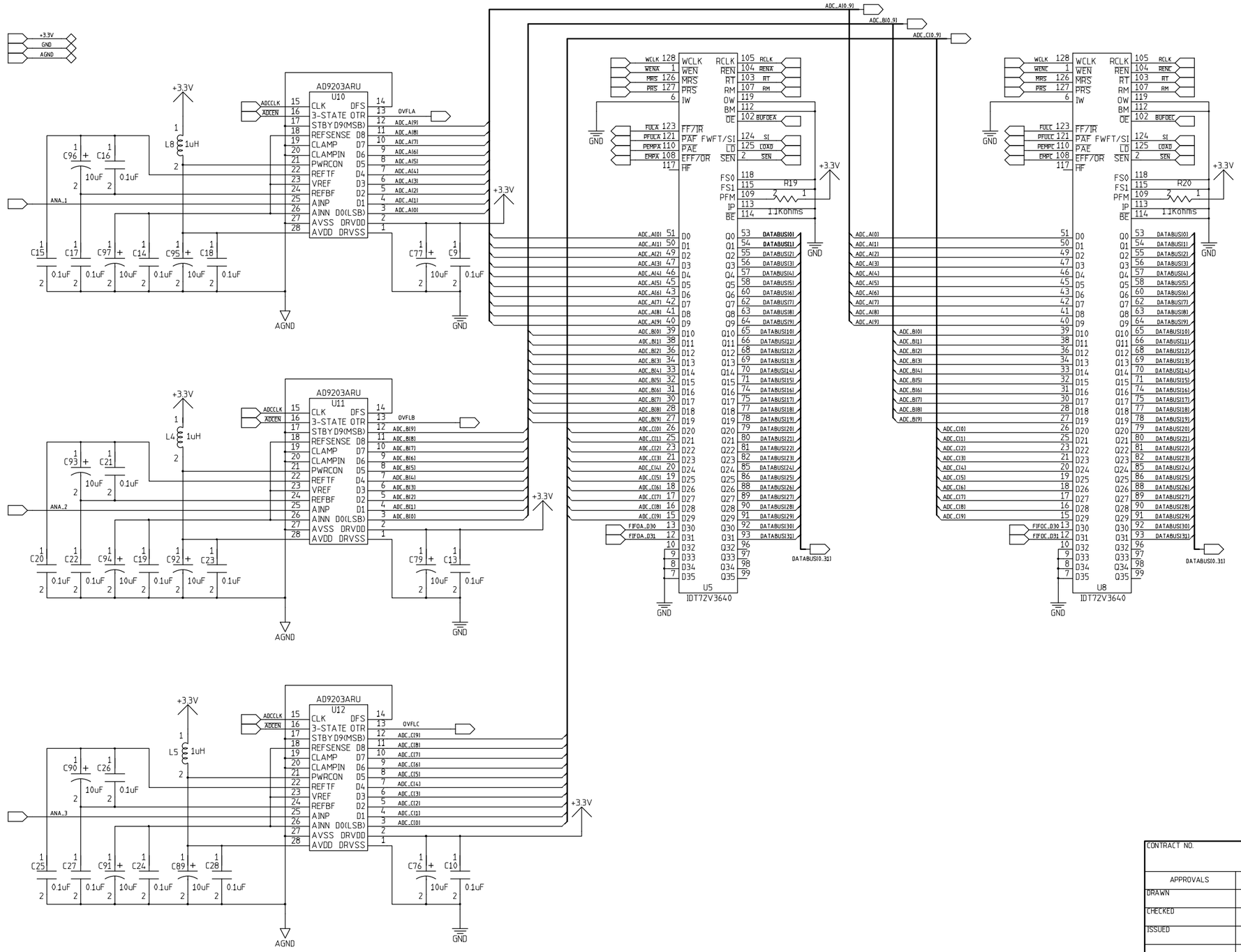


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

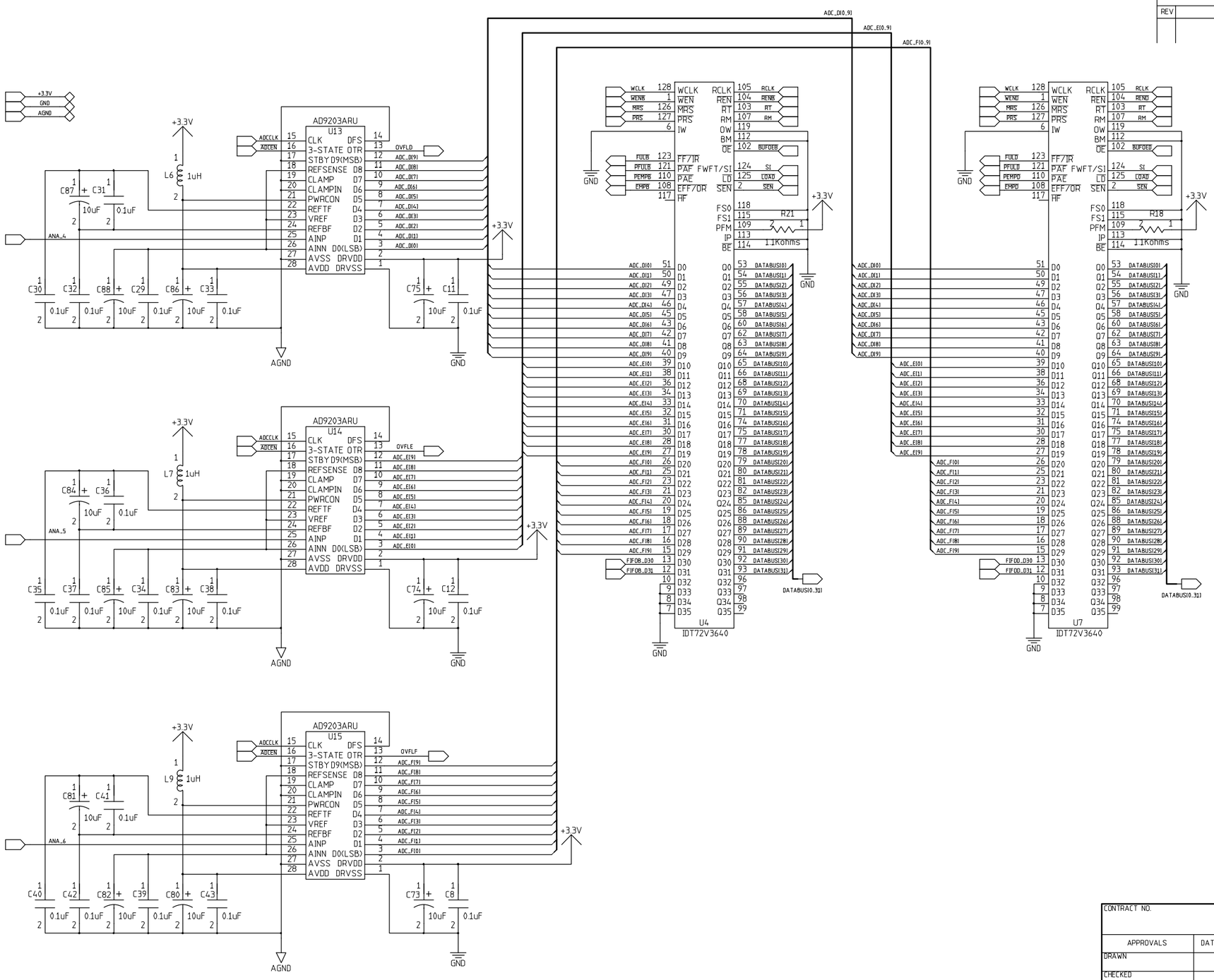
CONTRACT NO.		
APPROVALS	DATE	High Gain Channel ADCs and FIFOs
DRAWN		
CHECKED		
ISSUED		SIZE D · FSCM NO. · DWG. NO. · REV
		SCALE · SHEET

H=8ND; V=4LA

DWG. NO.	SH.	REV

REVISIONS

REV	DESCRIPTION	DATE	APPROVED

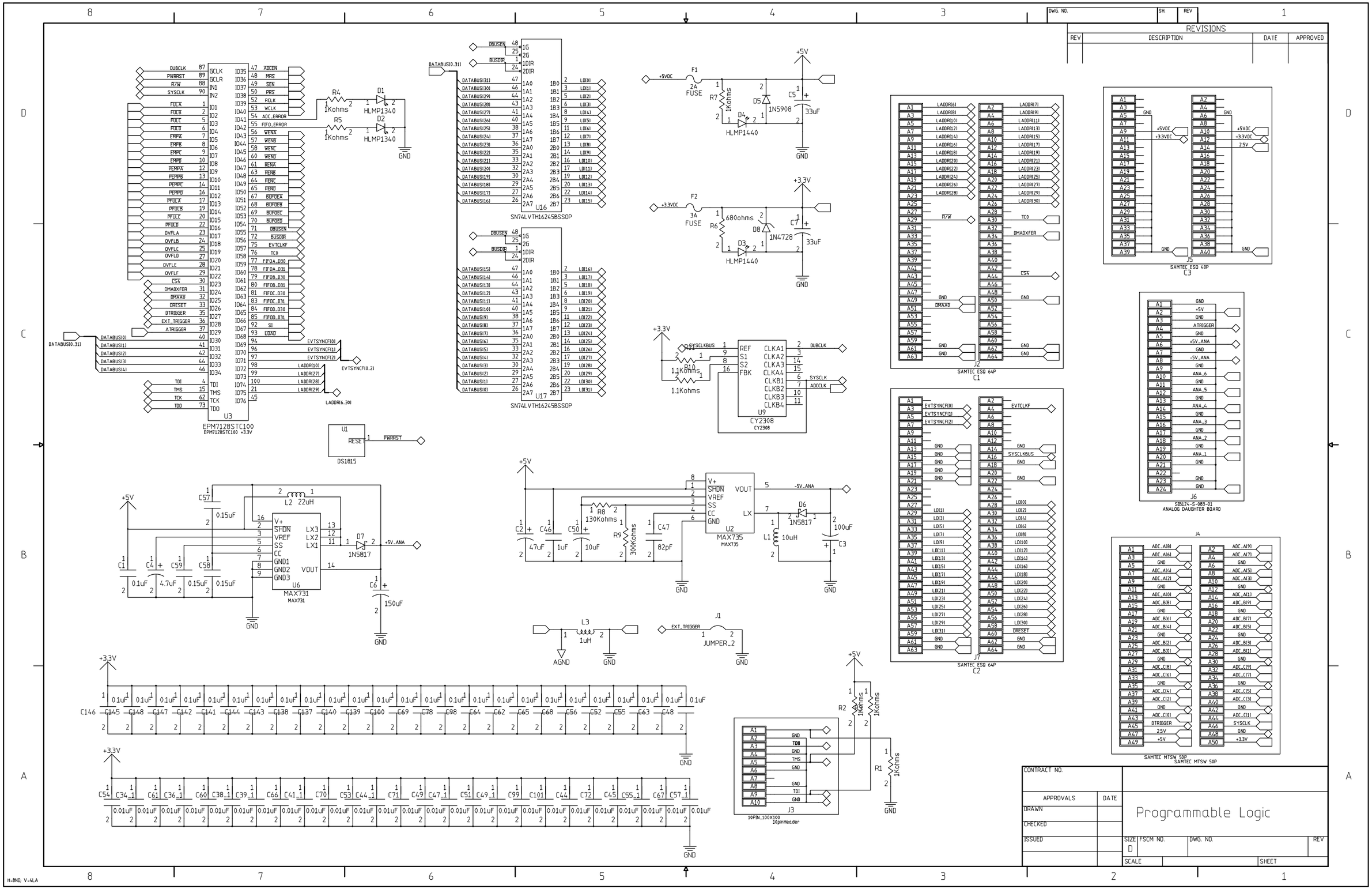


CONTRACT NO.	
APPROVALS	DATE
DRAWN	
CHECKED	
ISSUED	

Low Gain Channel
ADCs and FIFOs

SIZE	FSCM NO.	DWG. NO.	REV
D			
SCALE		SHEET	

H=BND; V=4LA



REVISIONS
REV
DESCRIPTION
DATE
APPROVED
DWG. NO.
SH.
REV
EPM7128STC100
EPM7128STC100 +3.3V
U3
SN74LVTH16245BSSOP
U16
SN74LVTH16245BSSOP
U17
CY2308
CY2308
U9
DS1815
U1
MAX731
MAX731
U6
MAX735
MAX735
U2
SAMTEC ESQ 64P
C1
SAMTEC ESQ 64P
C2
SAMTEC ESQ 40P
C3
SIB124-S-083-01
ANALOG DAUGHTER BOARD
SAMTEC MTSW 50P
SAMTEC MTSW 50P
10PIN_100X100
10pinHeader
J1
JUMPER_2
J3
J4
J5
J6
J7
J2
CONTRACT NO.
APPROVALS
DATE
DRAWN
CHECKED
ISSUED
Programmable Logic
SIZE
D
FSCM NO.
DWG. NO.
REV
SCALE
SHEET
H=8ND; V=4LA